

contain easy-to-follow mathematical derivations of all equations and formulas, graphical plots, and open-ended design problems to help determine most suitable architecture for a given set of performance specifications. This comprehensive and illustrative text for the design and analysis of CMOS analog integrated circuits serves as a valuable resource for analog circuit designers and graduate students in electrical engineering.

Temperature- and Supply Voltage-Independent Time References for Wireless Sensor NetworksSpringer

This book is a comprehensive, interdisciplinary resource for the latest information on implantable medical devices, and is intended for graduate students studying electrical engineering, electronic instrumentation, and biomedical engineering. It is also appropriate for academic researchers, professional engineers, practicing doctors, and paramedical staff. Divided into two sections on Basic Concepts and Principles, and Applications, the first section provides an all-embracing perspective of the electronics background necessary for this work. The second section deals with pacing techniques used for the heart, brain, spinal cord, and the network of nerves that interlink the brain and spinal cord with the major organs, including ear and eye prostheses. The four main offshoots of implantable electronics, which this book discusses, are: The insertion of an implantable neural amplifier for accurate recording of neural signals for neuroengineering studies The use of implantable pulse generators for pacing the activities of diseased organs The use of implantable sensors for observing the influence of therapy and monitoring a patient's biological parameters The use of drug delivery systems to supervise the supply of accurate doses of medicine to affected parts Readers will also find chapters on the essentials of clocking and timing circuits, pulse generator circuits, neural amplifiers, batteries, biomaterials and biocompatibility, and more. Unique to this book is also a chapter on cyber security and confidentiality concerns with implants. End-of-chapter questions and exercises help readers apply the content to practical use, making this an ideal book for anyone wishing to learn more about implantable devices.

VLSI Design and Test

The Engineer's Digest

Device Design, Process Integration, Characterization, and Reliability

Implantable Medical Electronics

Superior-Order Curvature-Correction Techniques for Voltage References

Analog Circuit Design Techniques at 0.5V

This invaluable second volume of a two-volume set is filled with details about the integrated circuit design for space applications. Various considerations for the selection and application of electronic components for designing spacecraft are discussed. The basic constructions of submicron transistors and schottky diodes during the technological process of production are explored. This book provides details on the energy consumption minimization methods for microelectronic devices. Specific topics include: Features and physical mechanisms of the effect of space radiation on all the main classes of microcircuits, including peculiarities of radiation impact on submicron integrated circuits;Special design, technology, and schematic methods of increasing the resistance to various types of space radiation;Recommendations for choosing research equipment and methods for irradiating various samples;Microcircuit designers on the composition of test elements for the study of the effect of radiation;Microprocessors, circuit boards, logic microcircuits, digital, analog, digital-analog microcircuits manufactured in various technologies (bipolar, CMOS, BiCMOS, SOI);Problems involved with designing high speed microelectronic devices and systems based on SOS-and SOI-structures;System-on-chip and system-in-package and methods for rejection of silicon microcircuits with hidden defects during mass production.

MicroCMOS Design covers key analog design methodologies with an emphasis on analog systems that can be integrated into systems-on-chip (SoCs). Starting at the transistor level, this book introduces basic concepts in the design of system-level complementary metal-oxide semiconductors (CMOS). It uses practical examples to illustrate circuit construction so that readers can develop an intuitive understanding rather than just assimilate the usual conventional analytical knowledge. As SoCs become increasingly complex, analog/radio frequency (RF) system designers have to master both system- and transistor-level design aspects. They must understand abstract concepts associated with large components, such as analog-to-digital converters (ADCs) and phase-locked loops (PLLs). To help readers along, this book discusses topics including: Amplifier basics & design Operational amplifier (Opamp) Data converter basics Nyquist-rate data converters Oversampling data converters High-resolution data converters PLL basics Frequency synthesis and clock recovery Focused more on design than analysis, this reference avoids lengthy equations and instead helps readers acquire a more hands-on mastery of the subject based on the application of core design concepts. Offering the needed perspective on the various design techniques for data converter and PLL design, coverage starts with abstract concepts—including discussion of bipolar junction transistors (BJTs) and MOS transistors—and builds up to an examination of the larger systems derived from microCMOS design.

Time-mode circuits, where information is represented by time difference between digital events, offer a viable and technology-friendly means to realize mixed-mode circuits and systems in nanometer complementary metal-oxide semiconductor (CMOS) technologies. Various architectures of time-based signal processing and design techniques of CMOS time-mode circuits have emerged; however, an in-depth examination of the principles of time-based signal processing and design techniques of time-mode circuits has not been available—until now. CMOS Time-Mode Circuits and Systems: Fundamentals and Applications is the first book to deliver a comprehensive treatment of CMOS time-mode circuits and systems. Featuring contributions from leading experts, this authoritative text contains a rich collection of literature on time-mode circuits and systems. The book begins by presenting a critical comparison of voltage-mode, current-mode, and time-mode signaling for mixed-mode signal processing and then: Covers the fundamentals of time-mode signal processing, such as voltage-to-time converters, all-digital phase-locked loops, and frequency synthesizers Investigates the performance characteristics, architecture, design techniques, and implementation of time-to-digital converters Discusses time-mode delta-sigma-based analog-to-digital converters, placing a great emphasis on time-mode quantizers Includes a detailed study of ultra-low-power integrated time-mode temperature measurement systems CMOS Time-Mode Circuits and Systems: Fundamentals and Applications provides a valuable reference for circuit design engineers, hardware system engineers, graduate students, and others seeking to master this fast-evolving field.

This book constitutes the refereed proceedings of the 8th IFIP WG 5.5/SOCOLNET Advanced Doctoral Conference on Computing, Electrical and Industrial Systems, DoCEIS 2017, held in Costa de Caparica, Portugal, in May 2017. The 46 revised full papers were carefully reviewed and selected from 95 submissions. The papers present selected results produced in engineering doctoral programs and focus on technological innovation for smart systems. Research results and ongoing work are presented, illustrated and discussed in the following areas: collaborative networks, computational intelligence, systems analysis, smart manufacturing systems, smart sensorial systems, embedded and real time systems, energy: management, energy: optimization, distributed infrastructure, solar energy, electrical machines, power electronics, and electronics.

The Reliability Handbook

A Sub 1 V Bandgap Reference Circuit

Amplifiers, Comparators, Multipliers, Filters, and Oscillators

Electronics and Microprocessors

Silicon Analog Components

8th IFIP WG 5.5/SOCOLNET Advanced Doctoral Conference on Computing, Electrical and Industrial Systems, DoCEIS 2017, Costa de Caparica, Portugal, May 3-5, 2017, Proceedings

CMOS MOS

Praise for CMOS: Circuit Design, Layout, and Simulation*Revised Second Edition from the Technical Reviewers "A refreshing industrial flavor. Design concepts are presented as they are needed for 'just-in-time' learning. Simulating and designing circuits using SPICE is emphasized with literally hundreds of examples. Very few textbooks contain as much detail as this one. Highly recommended!" --Paul M. Furth, New Mexico State University "This book builds a solid knowledge of CMOS circuit design from the ground up. With coverage of process integration, layout, analog and digital models, noise mechanisms, memory circuits, references, amplifiers, PLLs/DLLs, dynamic circuits, and data converters, the text is an excellent reference for both experienced and novice designers alike." --Tyler J. Gomm, Design Engineer, Micron Technology, Inc. "The Second Edition builds upon the success of the first with new chapters that cover additional material such as oversampled converters and non-volatile memories. This is becoming the de facto standard textbook to have on every analog and mixed-signal designer's bookshelf." --Joe Walsh, Design Engineer, AMI Semiconductor CMOS circuits from design to implementation CMOS: Circuit Design, Layout, and Simulation, Revised Second Edition covers the practical design of both analog and digital integrated circuits, offering a vital, contemporary view of a wide range of analog/digital circuit blocks, the BSIM model, data converter architectures, and much more. This edition takes a two-path approach to the topics: design techniques are developed for both long- and short-channel CMOS technologies and then compared. The results are multidimensional explanations that allow readers to gain deep insight into the design process. Features include: Updated materials to reflect CMOS technology's movement into nanometer sizes Discussions on phase- and delay-locked loops, mixed-signal circuits, data converters, and circuit noise More than 1,000 figures, 200 examples, and over 500 end-of-chapter problems In-depth coverage of both analog and digital circuit-level design techniques Real-world process parameters and design rules The book's Web site, CMOSedu.com, provides: solutions to the book's problems; additional homework problems without solutions; SPICE simulation examples using HSPICE, LTspice, and WinSpice; layout tools and examples for actually fabricating a chip; and videos to aid learning This book covers modern analog components, their characteristics, and interactions with process parameters. It serves as a comprehensive guide, addressing both the theoretical and practical aspects of modern silicon devices and the relationship between their electrical properties and processing conditions. Based on the authors' extensive experience in the development of analog devices, this book is intended for engineers and scientists in semiconductor research, development and manufacturing. The problems at the end of each chapter and the numerous charts, figures and tables also make it appropriate for use as a text in graduate and advanced undergraduate courses in electrical engineering and materials science. Enables engineers to understand analog device physics, and discusses important relations between process integration, device design, component characteristics, and reliability; Describes in step-by-step fashion the components that are used in analog designs, the particular characteristics of analog components, while comparing them to digital applications; Explains the second-order effects in analog devices, and trade-offs between these effects when designing components and developing an integrated process for their manufacturing.*

This book is about various adaptive and dynamic techniques used to optimize processor power and performance. It is based on a very successful forum at ISSCC which focused on Adaptive Techniques. The book looks at the underlying process technology for adaptive designs and then examines different circuits, architecture and software that address the different aspects. The chapters are written by people both in academia and the industry to show the scope of alternative practices.

Prosthetics, Drug Delivery, and Health Monitoring

CMOS Time-Mode Circuits and Systems

Circuit Design, Layout, and Simulation

Theory and Practice

CMOS Analog Integrated Circuits

Patents

The book will address the-state-of-the-art in integrated circuit design in the context of emerging systems. New exciting opportunities in body area networks, wireless communications, data networking, and optical imaging are discussed. Emerging materials that can take system performance beyond standard CMOS, like Silicon on Insulator (SOI) (SiGe), and Indium Phosphide (InP) are explored. Three-dimensional (3-D) CMOS integration and co-integration with sensor technology are described as well. The book is a must for anyone serious about circuit design for future technologies. The book is written by top notch international experts in industry and academia. The intended audience is those with integrated circuit background. The book will be also used as a recommended reading and supplementary material in graduate course curriculum. Intended audience is professionals working in the integrated circuit design field. Their job titles might be : design engineer, product manager, marketing manager, design team leader, etc. The book is also suitable for graduate students. Many of the chapter authors are University Professors.

CMOS Data Converters for Communications distinguishes itself from other data converter books by emphasizing system-related aspects of the design and frequency-domain measures. It explains in detail how to derive data converter requirements for a given communication system (baseband, passband, and multi-carrier systems). The author discusses various converter architectures and discuss their suitability for communications. The rest of the book is dedicated to high-performance CMOS data converter architecture and circuit design. Pipelined ADCs, parallel ADCs with an improved passive sampling technique, and oversampling ADCs are the focus for ADC architectures, while current-steering DACs are the focus for DAC architectures. The principles of the switched-current and the switched-capacitor techniques are reviewed and their applications to crucial functional blocks such as multiplying DACs and integrators are detailed. The book outlines the design of the basic building blocks such as operational amplifiers, comparators, and pulse generators with emphasis on the practical aspects. To operate analog circuits at a reduced supply voltage, special circuit techniques are needed. Low-voltage techniques are also discussed in this book. CMOS Data Converters for Communications can be used as a reference book by analog circuit designers to understand the data converter requirements for communication applications. It can also be used by telecommunication system designers to understand the difficulties of certain performance requirements on data converters. It is also an excellent resource to prepare analog students for the new challenges ahead.

This book presents Volume 1 of selected research papers presented at the Second International Conference on Digital Technologies and Applications (ICDTA 22), held at Sidi Mohamed Ben Abdellah University, Fez, Morocco, on January 28-30, 2022. This book highlights the latest innovations in digital technologies as: artificial intelligence, Internet of Things (IoT), smart systems, network technology, information processing and their applications in several areas as hybrid vehicles, renewable energy, mechatronics, medicine... This book will encourage and inspire researchers, industry professionals, and policymakers to put these methods into practice.

Biopotential Readout Circuits for Portable Acquisition Systems describes one of the main building blocks of such miniaturized biomedical signal acquisition systems. The focus of this book is on the implementation of low-power and high-performance integrated circuit building blocks that can be used to extract biopotential signals from conventional systems. New instrumentation amplifier architectures are introduced and their design is described in detail. These amplifiers are used to implement complete acquisition demonstrator systems that are a stepping stone towards practical miniaturized and low-power systems.

VLSI Reference Circuits - Theory, Design, and Applications

Variation Aware Analog and Mixed-Signal Circuit Design in Emerging Multi-Gate CMOS Technologies

IEICE Transactions on Electronics

Physically Unclonable Functions (PUFs)

Basic Concepts, Algorithms, and Applications

Recent Approaches for Designing Green High Performance Computing