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This junior level electronics text provides a foundation for analyzing

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and designing analog and digital electronics throughout the book. Extensive pedagogical features including numerous design examples, problem solving technique sections, Test Your Understanding questions, and

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chapter checkpoints lend to this classic text. The author, Don Neamen, has many years experience as an Engineering Educator. His experience shines through each chapter of the book, rich with realistic examples and

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chapter to the material presented in previous chapters. The objectives of the chapter are then presented in the Preview section and then are listed in bullet form for easy reference. Test Your Understanding Exercise Problems with provided

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answers have all been updated. Design Applications are included at the end of chapters. A specific electronic design related to that chapter is presented. The various stages in the design of an electronic thermometer are

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explained throughout the text. Specific Design Problems and Examples are highlighted throughout as well.

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number of examples, including new design examples, has been increased, giving students more opportunity to see problems worked out. Additionally, some of the less fundamental mathematical material has been

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and the discrete Fourier transforms, and the z-transform. The text integrates MATLAB examples into the presentation of signal and system theory and applications. Many interesting design trends

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are shown by the six papers on operational amplifiers (Op Amps). Firstly, there is the line of stand-alone Op Amps using a bipolar IC technology which combines high-frequency and high voltage. This line is

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frequency behaviour of the stages of a two-stage Op Amp. Both papers also present trends in current-mode feedback Op Amps. Low-voltage bipolar Op Amp design is presented by leroen Fonderie. He shows how

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multipath nested Miller compensation can be applied to turn rail-to-rail input and output stages into high quality low-voltage Op Amps. Two papers on CMOS Op Amps by Michael Steyaert and Klaas Bult show

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how high speed and high gain VLSI building blocks can be realised. Without departing from a single-stage OT A structure with a folded cascode output, a thorough high frequency design technique and

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a gain-boosting technique contributed to the high-speed and the high-gain achieved with these Op Amps. . Finally. Rinaldo Castello shows us how to provide output power with CMOS buffer amplifiers. The

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combination of class A and AB stages in a multipath nested Miller structure provides the required linearity and bandwidth.

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