

How To Set The Timing For Suzuki F6a 12 Valve Multicab Fixya

A wide-ranging and practical handbook that offers comprehensive treatment of high-pressure common rail technology for students and professionals. In this volume, Dr. Ouyang and his colleagues answer the need for a comprehensive examination of high-pressure common rail systems for electronic fuel injection technology, a crucial element in the optimization of diesel engine efficiency and emissions. The text begins with an overview of common rail systems today, including a look back at their progress since the 1970s and an examination of recent advances in the field. It then provides a thorough grounding in the design and assembly of common rail systems with an emphasis on key aspects of their design and assembly, as well as notable technological innovations. This includes discussion of advancements in dual pressure common rail systems and the increasingly influential role of Electronic Control Unit (ECU) technology in fuel injector systems. The authors conclude with a look towards the development of a new type of common rail system. Throughout the volume, concepts are illustrated using extensive research, experimental studies and simulation. Topics covered include: Comprehensive detailing of common rail system elements, elementary enough for newcomers and thorough enough to act as a useful

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reference for professionals Basic and simulation model of common rail systems, including extensive instruction performing simulations and analyzing key performance parameters Examination of the design and testing of n generation twin common rail systems, including applications for marine diesel engines Discussion of current trends in industry research as well as areas requiring further study Common Rail Fuel Injection Technology is the ideal handbook for students and professionals working in advanced automotive engineering, particularly researchers and engineers focused on the design of internal combustion engines advanced fuel injection technology. Wide-ranging research and ample examples of practical applications will make this a valuable resource both in education and private industry.

Timing, timing, timing! That is the main concern of a digital designer charged with designing a semiconductor chip. What is it, how is it described, and how does one verify it? The design team of a large digital design may spend months architecting and iterating the design to achieve the required timing target. Besides functional verification, the timing closure is the major milestone which dictates when a chip can be released to the semiconductor foundry for fabrication. This book addresses the timing verification using static timing analysis for nanometer designs. The book has originated from many years of our working in the area of timing verification for complex nanometer designs. We have

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come across many design engineers trying to learn the background and various aspects of static timing analysis. Unfortunately, there is no book currently available that can be used by a working engineer to get acquainted with the - tails of static timing analysis. The chip designers lack a central reference for information on timing, that covers the basics to the advanced timing verification procedures and techniques.

An interactive guide to Oracle's intensive query tool, SQL* Plus, discusses its powerful features, furnishes a syntax quick reference, and explains how to write and execute script files, generate reports, extract data from the database, utilize new administrative features, query data dictionary tables, and more. Original. (Intermediate)

Advanced Rails

Gravitational Wave Detection and Data Analysis for Pulsar Timing Arrays

Static Timing Analysis for Nanometer Designs

How to Specify, Program, and Verify Systems in Rewriting Logic

Create high quality videos for YouTube and other social media platforms with Blender

The Definitive Guide

Despite the richness of the subject and the importance frequently ascribed to the phenomena of rhythm and timing in the arts, the topic as a whole has been neglected. Janet Goodridge writes from

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a practical movement background and draws on a wide range of sources to illuminate the subject in relation to theatre, drama, dance, ceremony, and ritual.

This book contains extended and revised versions of the best papers presented at the 28th IFIP WG 10.5/IEEE

International Conference on Very Large Scale Integration, VLSI-SoC 2020, held in Salt Lake City, UT, USA, in October 2020. The 16 full papers included in this volume were carefully reviewed and selected from the 38 papers (out of 74 submissions) presented at the conference. The papers discuss the latest academic and industrial results and developments as well as future trends in the field of System-on-Chip (SoC) design, considering the challenges of nano-scale, state-of-the-art and emerging manufacturing technologies. In particular they address cutting-edge research fields like low-power design of RF, analog and mixed-signal circuits, EDA tools for the synthesis and verification of heterogenous SoCs, accelerators for cryptography and deep learning and on-*

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*chip Interconnection system, reliability and testing, and integration of 3D-ICs. *The conference was held virtually.*

The Asia and South Pacific conference on design automation is the second in a series of biennial international conferences. It aims to provide the CAD/DA community with the opportunity to present ideas and concepts on upstream design as well as methodologies of downstream design.

Rhythm and Timing of Movement in Performance

Building Industrial-Strength Web Apps in Record Time

Major Collective Bargaining Agreements Wage-incentive, Production-standard, and Time-study Provisions

Proceedings of the ASP-DAC '97, Asia and South Pacific Design Automation Conference 1997, January 28-31, 1997, Makuhari Messe, Nippon Convention Center, Chiba, Japan

Dyke's Automobile and Gasoline Engine Encyclopedia

A Dictionary for Strangers in a Strange Land

An elegant and counterintuitive guide to achieving

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perfecttiming Timing is everything. Whether we are making strategic businessdecisions or the smallest personal choice, we must decide not onlywhat to do, but when to do it. Act too early—ortoo late—and the results can be disastrous. Based on a20-year investigation into more than 2,000 timing issues anderrors, When presents a single and practical approach fordealing with timing in life and business. Good timing, Albertargues, is not just a matter of luck, intuition, or pastexperience—all of which may be unreliable—but a skill.He describes that skill and details the tools and methods needed toconduct a successful timing analysis. The book is the first to offer an efficient and comprehensiveway to think through any timing issue Filled with dozens of lively stories illustrating good and badtiming in all walks of life—business, warfare, medicine,sports, entertainment and the arts Written by Stuart Albert, one of the foremost timing experts inthe world and developer of the first practical, research-basedmethod for turning the skill of timing into a competitiveadvantage Engaging and counterintuitive, When will show everyone,regardless of the work they do, or the life they live, that "it'sall in the timing."

The instant New York Times Bestseller #1 Wall Street Journal Business Bestseller Instant Washington Post Bestseller "Brimms with a surprising amount of insight and practical advice." --The Wall

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Street Journal Daniel H. Pink, the #1 bestselling author of *Drive* and *To Sell Is Human*, unlocks the scientific secrets to good timing to help you flourish at work, at school, and at home. Everyone knows that timing is everything. But we don't know much about timing itself. Our lives are a never-ending stream of "when" decisions: when to start a business, schedule a class, get serious about a person. Yet we make those decisions based on intuition and guesswork. Timing, it's often assumed, is an art. In *When: The Scientific Secrets of Perfect Timing*, Pink shows that timing is really a science. Drawing on a rich trove of research from psychology, biology, and economics, Pink reveals how best to live, work, and succeed. How can we use the hidden patterns of the day to build the ideal schedule? Why do certain breaks dramatically improve student test scores? How can we turn a stumbling beginning into a fresh start? Why should we avoid going to the hospital in the afternoon? Why is singing in time with other people as good for you as exercise? And what is the ideal time to quit a job, switch careers, or get married? In *When*, Pink distills cutting-edge research and data on timing and synthesizes them into a fascinating, readable narrative packed with irresistible stories and practical takeaways that give readers compelling insights into how we can live richer, more engaged lives.

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Recent years have seen rapid strides in the level of sophistication of VLSI circuits. On the performance front, there is a vital need for techniques to design fast, low-power chips with minimum area for increasingly complex systems, while on the economic side there is the vastly increased pressure of time-to-market. These pressures have made the use of CAD tools mandatory in designing complex systems. Timing Analysis and Optimization of Sequential Circuits describes CAD algorithms for analyzing and optimizing the timing behavior of sequential circuits with special reference to performance parameters such as power and area. A unified approach to performance analysis and optimization of sequential circuits is presented. The state of the art in timing analysis and optimization techniques is described for circuits using edge-triggered or level-sensitive memory elements. Specific emphasis is placed on two methods that are true sequential timing optimizations techniques: retiming and clock skew optimization. Timing Analysis and Optimization of Sequential Circuits covers the following topics: Algorithms for sequential timing analysis Fast algorithms for clock skew optimization and their applications Efficient techniques for retiming large sequential circuits Coupling sequential and combinational optimizations. Timing Analysis and Optimization of Sequential Circuits is written for graduate students,

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researchers and professionals in the area of CAD for VLSI and VLSI circuit design.

When: The Scientific Secrets of Perfect Timing

Timing and Telephone Set ML-110

Drama, Dance and Ceremony

A Systematic Approach to Drum Set Fundamentals

Henry Holden Huss

Timing Is Everything

CROPWAT

Ready to go to the next level with Rails? From examining the parts of Ruby that make this framework possible to deploying large Rails applications, Advanced Rails offers you an in-depth look at techniques for dealing with databases, security, performance, web services and much more. Chapters in this book help you understand not only the tricks and techniques used within the Rails framework itself, but also how to make use of ideas borrowed from other programming paradigms. Advanced Rails pays particular attention to building applications that scale -- whether "scale" means handling more users, or working with a bigger and more complex database. You'll find plenty of examples and code samples that explain: Aspects of Ruby that are often confusing or misunderstood Metaprogramming How to develop Rails plug-ins Different database management systems Advanced database features, including triggers, rules, and stored procedures How to connect to multiple databases When to use the Active Support library for generic, reusable functions Security principles for web application design, and security issues endemic to the Web When and when not to optimize performance Why version control and issue tracking systems are essential to any large or long-lived Rails project

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Advanced Rails also gives you a look at REST for developing web services, ways to incorporate and extend Rails, how to use internationalization, and many other topics. If you're just starting out with rails, or merely experimenting with the framework, this book is not for you. But if you want to improve your skills with Rails through advanced techniques, this book is essential.

Setting up cropwat: conditions of use and distribution; Installation; File structure. Getting started on cropwat: Main menu; Printer setting; Default drive and path for data input and retrieval. Eto Calculations: Input climatic station; Retrieval of Eto data from disk; Output. Crop water requeriments: Input climate data; Input of crop data; Crop water requirements results; Program continuation. Rice water requirements: Input of rice data from keyboard; Saving of rice data; Retrieval of rice data from disk; Date of transplanting; Rice irrigation calculations; Rice irrigation output; Program continuation. Irrigation Scheduling: Data input for irrigation scheduling; Irrigation Scheduling options; Irrigation Scheduling calculations; Irrigation Scheduling output; Program continuation. Scheme water supply: Data input; Calculations; Output. Calculation of reference evapotranspiration: Data collection; Data conversion; Climatic data input; Climatic data and Eto output; Climatic and Eto data saving; Processing of rainfall data: Rainfall definitions; Rain data collection; Rain data processing; Effective Rainfall method; Rain data input; Saving climatic data; Printout of climatic data. Cropping pattern and crop information: Data collection; Cropping pattern; Crop data input. Crop water requirement calculations: File input; CWR calculations; CWR field files; Summary of CWR calculations. Scheme and canal water requirements: Calculation procedures; Field file input; Scheme water requirements (SWR) results; Evaluation of SWR results. Irrigation

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Scheduling: Data input; Soil data collection; Soil data input; Irrigation scheduling applications.

Resource added for the Automotive Technology program 106023.

11B10 - Infantryman - 11B20, Skill Levels 1,2

Speech Timing

Course Manual for U. S. Navy Liberty Motor School Course

Blender for Video Production Quick Start Guide

A Computer Program for Irrigation Planning and Management

Novel Algorithms and Techniques in Telecommunications, Automation and Industrial Electronics

Interval Timing and Time-Based Decision Making

Oracle SQL*Plus The Definitive Guide"O'Reilly Media, Inc."

Provides a definitive guide to terminology, techniques, and system information for individuals working in both Macintosh and Windows environments, explaining how to translate materials effectively from the one platform to the other. Original. (All Users)

This book covers GarageBand 10.0. The content and/or media files do not work with subsequent releases of the software. In the only Apple-certified guide to GarageBand 10.0.0, readers will be creating original works within the first few chapters. Using real-life material and practical lessons that they can apply immediately to their own projects, this book/media combo offers a complete, self-paced course in all aspects of GarageBand. Focused lessons take you step-by-step through fun, real-world projects, and GarageBand 10.0.0 features. Exclusively for this book, author/musician Mary Plummer works with a host of talented artists ranging from a student songwriter to a

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professional touring guitarist, an award-winning poet, an independent hip hop recording artist, and award winning dancers to create all new, real-world projects that readers will step-through. Along the way readers will get to mix a songwriter's demo, test amp simulators and stomp boxes with an electric guitar solo, edit spoken dialog for an audio book, lay down original hip-hop beats using a drum machine patch and dynamic tempo changes, and add percussion and effects to a dance video to enhance the sound. For mobile users, the book includes an overview of GarageBand for iOS and sharing GarageBand songs via iCloud between iOS devices and your Mac. This self-paced learning tool pairs an easy, accessible style with ample illustrations and keyboard shortcuts to guarantee that readers become proficient with GarageBand 10.0.0 in no time.

Crew Operation and First Echelon Through Fifth Echelon Maintenance for Compressor, Reciprocating, Power Driven, 100 CFM M3 (E35).

A Practical Approach

Timing Analysis and Optimization of Sequential Circuits Cache, DRAM, Disk

Timing Grace

Implications for Theories of Phonology, Speech Production, and Speech Motor Control

An American Composer's Life

In this companion volume to *A Space for Grace*, Johan Cilliers delves into some of the most profound theological underpinnings of preaching. Drawing on his extraordinary depth and breadth of scholarship, Cilliers examines the aesthetic, qualitative, and

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relational dimensions of sermonic time. Faithful preaching, he argues, is an art of speaking the now of grace, which is inextricably linked to past and future, but is simultaneously a dynamic event filled with the revelatory presence of God. Along with helpful reflections on pieces of visual art, Cilliers provides numerous sample sermons, as well as several detailed sermon analyses.

Is your memory hierarchy stopping your microprocessor from performing at the high level it should be? *Memory Systems: Cache, DRAM, Disk* shows you how to resolve this problem. The book tells you everything you need to know about the logical design and operation, physical design and operation, performance characteristics and resulting design trade-offs, and the energy consumption of modern memory hierarchies. You learn how to tackle the challenging optimization problems that result from the side-effects that can appear at any point in the entire hierarchy. As a result you will be able to design and emulate the entire memory hierarchy. Understand all levels of the system hierarchy -Xcache, DRAM, and disk. Evaluate the system-level effects of all design choices. Model performance and energy consumption for each component in the memory hierarchy.

This book explores the nature of cognitive representations and processes in speech motor control, based primarily on evidence from speech

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timing. It engages with the key question of whether phonological representations are spatio-temporal, as in the Articulatory Phonology approach, or symbolic (atemporal and non-quantitative); this issue has fundamental implications for the architecture of the speech production planning system, particularly with regard to the number of planning components and the type of timing mechanisms. Alice Turk and Stefanie Shattuck-Hufnagel outline a number of arguments in favour of an alternative to the Articulatory Phonology/Task Dynamics model. They demonstrate that a different framework is needed to account for evidence from speech and non-speech timing behaviour, and specifically that three separate planning components must be posited: Phonological Planning, Phonetic Planning, and Motor-Sensory Implementation. The approach proposed in the book provides a clearer and more comprehensive account of what is known about motor timing in general and speech timing in particular. It will be of interest to phoneticians and phonologists from all theoretical backgrounds as well as to speech clinicians and technologists.

Coil Ignition for Motor Cars

Common Rail Fuel Injection Technology in Diesel Engines

Digest of Technical Papers

Official Gazette of the United States Patent Office

GarageBand

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When

The Art of Timing Closure

th Welcome to the proceedings of PATMOS 2007, the 17 in a series of international workshops. PATMOS 2007 was organized by Chalmers University of Technology with IEEE Sweden Chapter of the Solid-State Circuit Society technical sponsorship and IEEE CEDA sponsorship. Over the years, PATMOS has evolved into an important European event, where - searchers from both industry and academia discuss and investigate the emerging ch- lenges in future and contemporary applications, design methodologies, and tools - quired for the development of the upcoming generations of integrated circuits and systems. The technical program of PATMOS 2007 consisted of state-of-the-art te- nical contributions, three invited talks and an industrial session on design challenges in real-life projects. The technical program focused on timing, performance and power consumption, as well as architectural aspects with particular emphasis on m- eling, design, characterization, analysis and optimization in the nanometer era. The Technical Program Committee, with the assistance of additional expert - viewers, selected the 55 papers presented at PATMOS. The papers were organized into 9 technical sessions and 3 poster sessions. As is always the case with the PATMOS workshops, full papers were required, and several reviews were received per manuscript. Maude is a language and system based on rewriting logic. In this comprehensive account, you'll discover how Maude and its formal tool environment can be used in three mutually reinforcing ways: as a declarative programming language, as an executable formal specification language, and as a formal verification system. Examples used throughout the book illustrate key concepts, features, and the many practical uses of Maude.

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Contains an annotated worklist, with suggestions of Huss pieces for further exploration by performers, a list of his writings on musical topics, and numerous illustrations.

The Whispering Roots

The Art of Perfect Timing

Patents

Advanced ASIC Design Implementation

A Manual for the Motor Mechanic, Owner-driver and All Interested in Coil Ignition Systems

All About Maude - A High-Performance Logical Framework

Soldier's Manual

The perception of time is crucial for everyday activities from the sleep-wake cycle to playing and appreciating music, verbal communication, to the determination of the value of a particular behavior. With regard to the last point, making decisions is heavily influenced by the duration of the various options, the duration of the expected delays for receiving the options, and the time constraints for making a choice. Recent advances suggest that the brain represents time in a distributed manner and reflects time as a result of temporal changes in network states and/or by the coincidence detection of the phase of different neural populations. Moreover, intrinsic oscillatory properties of neural circuits could determine timed motor responses. This Research Topic, partly an emergence of a Satellite EBBS

meeting sponsored by the COST-Action TIMELY, will discuss how time in the physical world is reconstructed, distorted and modified in brain networks by emotion, learning and neuropathology. This Research Topic on Timing contains up-to-date reviews regarding the relationship between time and decision-making with respect to the underlying psychological and physiological mechanisms responsible for anticipation and evaluation processes.

Pulsar timing is a promising method for detecting gravitational waves in the nano-Hertz band. In his prize winning Ph.D. thesis Rutger van Haasteren deals with how one takes thousands of seemingly random timing residuals which are measured by pulsar observers, and extracts information about the presence and character of the gravitational waves in the nano-Hertz band that are washing over our Galaxy. The author presents a sophisticated mathematical algorithm that deals with this issue. His algorithm is probably the most well-developed of those that are currently in use in the Pulsar Timing Array community. In chapter 3, the gravitational-wave memory effect is described. This is one of the first descriptions of this interesting effect in relation with

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pulsar timing, which may become observable in future Pulsar Timing Array projects. The last part of the work is dedicated to an effort to combine the European pulsar timing data sets in order to search for gravitational waves. This study has placed the most stringent limit to date on the intensity of gravitational waves that are produced by pairs of supermassive black holes dancing around each other in distant galaxies, as well as those that may be produced by vibrating cosmic strings. Rutger van Haasteren has won the 2011 GWIC Thesis Prize of the Gravitational Wave International Community for his innovative work in various directions of the search for gravitational waves by pulsar timing. The work is presented in this Ph.D. thesis.

Use Blender to edit and produce video for YouTube or any other social media platforms
Key Features Use the Blender Video editing toolkit and UI Make 3D info-graphics and interactive video with the latest Blender toolkit Prepare a video production with live markings for tracking Book Description One of the critical components of any workflow related to video production is a reliable tool to create and edit media such as video and audio. In most cases, you will find video

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producers using software that can only cut and mount video in a "traditional" way. What if you could use a software that offers not only options to edit and cut video, but also create 3D content and animation? With Blender, you can make use of a fantastic set of tools to edit and cut video, and also produce 3D content that will enable you to take your productions to the next level. Do you want to take footage from a camera and cut or add sound and titles? This book will show you how Blender can do that for you! You will learn to add 3D virtual objects to the same footage that will help you to create a full 3D environment. Using some camera tricks, you can even turn Blender into a powerful 2.5D animation software to create compelling infographics to produce educational, marketing, and instructional videos. You will also learn how to work with motion tracking to mix live-action footage with virtual objects. You will then learn how to use the video editing capabilities of Blender and match 3D content to your project for YouTube or any other media. Toward the end of the book, you will export the project to YouTube using optimal settings for the best performance in the platform. What you will learn Import video and audio footage to

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Blender Use the Video Sequencer Editor to manipulate footage Prepare a project related to video in Blender Cut and reorganize video footage in Blender Create animations and add voiceover and sound to video Build

infographics based on 3D content Blend 3D content with live-action footage Export video for YouTube using optimal settings Who this book is for Anyone trying to produce content based on video for platforms like YouTube. Those artists will need a software to cut and edit video footage or make small intro clips, animations, or info graphics for video.

Official Gazette of the United States Patent and Trademark Office

Serial set (no.8000-9000)

Oracle SQL*Plus

Crossing Platforms A Macintosh/Windows

Phrasebook

Memory Systems

American Blacksmith and Motor Shop

Automobile Journal

The Art of Timing Closure is written using a hands-on approach to describe advanced concepts and techniques using Multi-Mode Multi-Corner (MMMC) for an advanced ASIC design implementation. It focuses on the physical design, Static Timing Analysis (STA), formal and physical verification.

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The scripts in this book are based on Cadence® Encounter System™. However, if the reader uses a different EDA tool, that tool's commands are similar to those shown in this book. The topics covered are as follows: Data Structures Multi-Mode Multi-Corner Analysis Design Constraints Floorplan and Timing Placement and Timing Clock Tree Synthesis Final Route and Timing Design Signoff Rather than go into great technical depth, the author emphasizes short, clear descriptions which are implemented by references to authoritative manuscripts. It is the goal of this book to capture the essence of physical design and timing analysis at each stage of the physical design, and to show the reader that physical design and timing analysis engineering should be viewed as a single area of expertise. This book is intended for anyone who is involved in ASIC design implementation -- starting from physical design to final design signoff. Target audiences for this book are practicing ASIC design implementation engineers and students undertaking advanced courses in ASIC design.

Novel Algorithms and Techniques in Telecommunications, Automation and Industrial Electronics includes a set of

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rigorously reviewed world-class manuscripts addressing and detailing state-of-the-art research projects in the areas of Industrial Electronics, Technology and Automation, Telecommunications and Networking. Novel Algorithms and Techniques in Telecommunications, Automation and Industrial Electronics includes selected papers form the conference proceedings of the International Conference on Industrial Electronics, Technology and Automation (IETA 2007) and International Conference on Telecommunications and Networking (TeNe 07) which were part of the International Joint Conferences on Computer, Information and Systems Sciences and Engineering (CISSE 2007).

28th IFIP WG 10.5/IEEE International Conference on Very Large Scale Integration, VLSI-SoC 2020, Salt Lake City, UT, USA, October 6-9, 2020, Revised and Extended Selected Papers

Apple Pro Training Series

Reflections on the temporality of preaching

U.S. Marine Corps Maintenance Manual, Ordnance

VLSI-SoC: Design Trends

*Fundamentals of Automotive Technology
Integrated Circuit and System Design.*

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Power and Timing Modeling, Optimization and Simulation