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Provides students with an understanding of the modeling and practice in power system stability analysis and control design, as well as the computational tools used

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by commercial vendors Bringing together wind, FACTS, HVDC, and several other modern elements, this book gives readers everything they need to know about power systems. It makes learning complex power system concepts, models, and dynamics simpler and more efficient while providing modern viewpoints of power system analysis. Power System Modeling, Computation, and Control provides students with a new and detailed analysis of voltage stability; a simple example illustrating the BCU method of transient stability analysis; and one of only a few derivations of the transient synchronous machine model. It offers a

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discussion on reactive power consumption of induction motors during start-up to illustrate the low-voltage phenomenon observed in urban load centers. Damping controller designs using power system stabilizer, HVDC systems, static var compensator, and thyristor-controlled series compensation are also examined. In addition, there are chapters covering flexible AC transmission Systems (FACTS)—including both thyristor and voltage-sourced converter technology—and wind turbine generation and modeling. Simplifies the learning of complex power system concepts, models, and dynamics Provides chapters on power flow solution, voltage

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stability, simulation methods, transient stability, small signal stability, synchronous machine models (steady-state and dynamic models), excitation systems, and power system stabilizer design Includes advanced analysis of voltage stability, voltage recovery during motor starts, FACTS and their operation, damping control design using various control equipment, wind turbine models, and control Contains numerous examples, tables, figures of block diagrams, MATLAB plots, and problems involving real systems Written by experienced educators whose previous books and papers are used extensively by the international scientific

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community Power System Modeling, Computation, and Control is an ideal textbook for graduate students of the subject, as well as for power system engineers and control design professionals.

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Code of Federal Regulations

PC Magazine

Engineering IT-Enabled Sustainable Electricity Services

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Computer Design

System on Chip Interfaces for Low Power Design provides a top-down understanding of interfaces available to SoC developers, not only the underlying protocols and architecture of each, but also how they interact and the tradeoffs involved. The book offers a common context to help understand the variety of available interfaces and make sense of technology from different vendors aligned with multiple standards. With particular emphasis on power as a factor, the authors explain how each interface performs in various usage scenarios and discuss their advantages and disadvantages. Readers learn to make educated

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decisions on what interfaces to use when designing systems and gain insight for innovating new/custom interfaces for a subsystem and their potential impact. Provides a top-down guide to SoC interfaces for memory, multimedia, sensors, display, and communication Explores the underlying protocols and architecture of each interface with multiple examples Guides through competing standards and explains how different interfaces might interact or interfere with each other Explains challenges in system design, validation, debugging and their impact on development Want to learn how to create digital products on your home laptop or computer and sell them for profit? Digital

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product creation is one of the best ways to generate a fast online income. Not only are they easy to make and simple to sell, they offer a number of big advantages over physical products: - 100% profit, with the only cost the time taken to create them - Can be sold all over the world 24/7 even while you're asleep in bed - Endless inventory with no shop and no stock holding required Paul Teague has been making digital products for over a decade and has several 6-figure launches to his name. He's sold thousands of online training products and e-books in that time and is passionate about the power of online sales as one of the best ways to do business in the 21st Century. He's now condensed a decade of

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experience into one book. Topics covered in How To Create & Sell Digital Products include: - Why Create & Sell Digital Products? - Digital Formats & Types of Product - Essential Tools For Digital Product Creation - Digital Product Creations Tips & Techniques - Product Delivery Tools - Going It Alone With Product Delivery - Digital Sales Platforms - Taking Payments Directly - Promotional Strategies To Boost Digital Product Sales - Product Support Options - Digital Product Creation Next Steps - How to re-purpose existing content for profit If you want to know how to create digital products which sell using only free and low-cost tools, you need to start reading How To Create & Sell Digital Products today.

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The book provides a complete overview of the SIMATIC automation system and the TIA Portal with the engineering tool STEP 7. "Automating with SIMATIC" addresses all those who - want to get an overview of the components of the system and their features, - wish to familiarize themselves with the topic of programmable logic controllers, or - intend to acquire basic knowledge about configuration, programming and interaction of the SIMATIC components. At first, the book introduces the hardware of SIMATIC S7-1200, S7-300, S7-400 and S7-1500, including the ET 200 peripheral modules. This is followed by describing the work with STEP 7 in the programming languages LAD, FBD, STL, SCL and

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S7-Graph, and offline testing with S7-PLCSIM. The next section describes the structure of the user program, which is followed by the illustration of the data communication between the controllers of the automation system as well as with the peripheral devices by use of the bus systems Profinet and Profibus. The book closes with a survey of the devices for operator control and process monitoring and their configuration software.

PCI and PCI-X Hardware and Software

The Independent Guide to IBM-standard Personal Computing

Code Of Federal Regulations

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Peripheral Components

Power System Modeling, Computation, and Control

The most complete, authoritative technical guide to the FreeBSD kernel's internal structure has now been extensively updated to cover all major improvements between Versions 5 and 11. Approximately one-third of this edition's content is completely new, and another one-third has been extensively rewritten. Three long-time FreeBSD project leaders begin with a

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concise overview of the FreeBSD kernel's current design and implementation. Next, they cover the FreeBSD kernel from the system-call level down—from the interface to the kernel to the hardware. Explaining key design decisions, they detail the concepts, data structures, and algorithms used in implementing each significant system facility, including process management, security, virtual memory, the I/O system, filesystems,

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socket IPC, and networking. This Second Edition • Explains highly scalable and lightweight virtualization using FreeBSD jails, and virtual-machine acceleration with Xen and Virtio device paravirtualization • Describes new security features such as Capsicum sandboxing and GELI cryptographic disk protection • Fully covers NFSv4 and Open Solaris ZFS support • Introduces FreeBSD's enhanced volume management and new journaled soft updates •

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Explains DTrace's fine-grained process debugging/profiling • Reflects major improvements to networking, wireless, and USB support Readers can use this guide as both a working reference and an in-depth study of a leading contemporary, portable, open source operating system. Technical and sales support professionals will discover both FreeBSD's capabilities and its limitations. Applications developers will learn how to effectively and

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efficiently interface with it; system administrators will learn how to maintain, tune, and configure it; and systems programmers will learn how to extend, enhance, and interface with it. Marshall Kirk McKusick writes, consults, and teaches classes on UNIX- and BSD-related subjects. While at the University of California, Berkeley, he implemented the 4.2BSD fast filesystem. He was research computer scientist at the Berkeley Computer Systems Research

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Group (CSRG), overseeing development and release of 4.3BSD and 4.4BSD. He is a FreeBSD Foundation board member and a long-time FreeBSD committer. Twice president of the Usenix Association, he is also a member of ACM, IEEE, and AAAS. George V. Neville-Neil hacks, writes, teaches, and consults on security, networking, and operating systems. A FreeBSD Foundation board member, he served on the FreeBSD Core Team for four years. Since 2004, he has

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written the "Kode Vicious" column for Queue and Communications of the ACM. He is vice chair of ACM's Practitioner Board and a member of Usenix Association, ACM, IEEE, and AAAS. Robert N.M. Watson is a University Lecturer in systems, security, and architecture in the Security Research Group at the University of Cambridge Computer Laboratory. He supervises advanced research in computer architecture, compilers, program

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analysis, operating systems, networking, and security. A FreeBSD Foundation board member, he served on the Core Team for ten years and has been a committer for fifteen years. He is a member of Usenix Association and ACM.

For more than 40 years, Computerworld has been the leading source of technology news and information for IT influencers worldwide. Computerworld's award-winning Web site

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(Computerworld.com), twice-monthly publication, focused conference series and custom research form the hub of the world's largest global IT media network.

Control engineering seeks to understand physical systems, using mathematical modeling, in terms of inputs, outputs and various components with different behaviors. It has an essential role in a wide range of control systems, from household appliances to space flight.

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This book provides an in-depth view of the technologies that are implemented in most varieties of modern industrial control engineering. A solid grounding is provided in traditional control techniques, followed by detailed examination of modern control techniques such as real-time, distributed, robotic, embedded, computer and wireless control technologies. For each technology, the book discusses its full profile, from

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the field layer and the control layer to the operator layer. It also includes all the interfaces in industrial control systems: between controllers and systems; between different layers; and between operators and systems. It not only describes the details of both real-time operating systems and distributed operating systems, but also provides coverage of the microprocessor boot code, which other books lack. In addition to working principles and

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operation mechanisms, this book emphasizes the practical issues of components, devices and hardware circuits, giving the specification parameters, install procedures, calibration and configuration methodologies needed for engineers to put the theory into practice. Documents all the key technologies of a wide range of industrial control systems Emphasizes practical application and methods alongside theory and principles

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An ideal reference for practicing engineers needing to further their understanding of the latest industrial control concepts and techniques

Computerworld

Transportation

ISTCS '92, Israel Symposium, Haifa, Israel, May 27-28, 1992. Proceedings

Theory of Computing and Systems

Real-time Kernel Programming

The fastest, easiest, most comprehensive way to learn Adobe Audition CC Classroom in a Book®, the best-selling series of hands-

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on so ware training workbooks, offers what no other book or training program does—an official training series from Adobe, developed with the support of Adobe product experts. Adobe Audition CC Classroom in a Book contains 17 lessons that cover the basics and beyond, providing countless tips and techniques to help you become more productive with the program. You can follow the book from start to finish or choose only those lessons that interest you. Purchasing this book includes valuable online extras. Follow the instructions in the book’s “Getting Started” section to unlock access to:

- Downloadable lesson files you need to work through the projects in the book
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Adobe Audition CC software, for either Windows or macOS.

(Software not included.) Note: Classroom in a Book does not replace the documentation, support, updates, or any other benefits of being a registered owner of Adobe Audition CC software.

The peripheral component interconnect (PCI) bus is the dominant bus system used to connect the different elements making up today's high-performance computer systems. Different PCI implementations have also been developed for such applications as telecommunications and embedded computing. If an application calls for high speed, high reliability, flexible configuration, and bus mastering, then PCI is the only logical bus choice. This book is an applications-oriented introduction to the PCI bus, with an emphasis on implementing PCI in a variety of computer architectures. Special attention is given to industrial and mission-critical applications of

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PCI bus. ·Fully describes PCI electrical specifications, mechanical requirements, and signal types ·Covers advanced topics through numerous design examples to increase the readers understanding of the subject ·Includes updated coverage of PCI-X 2.0

This IBM Redbooks publication gives a broad understanding of the new System i5 architecture as it applies to logically partitioned System i5 systems. This functionality is delivered through a new configuration and management interface called the Hardware Management Console (HMC). Reading this book will help you design your server partition scheme from scratch. We also discuss the requirements to create a solution to migrate from existing iSeries servers with and without logical partitions. This book will help you install, tailor, and configure logical partitions (LPARs) on System i5 systems. You will understand how to set up the server

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and HMC via the Guided Setup Wizard. We also cover the creation of multiple i5/OS partitions, where the OS could be IBM i5/OS, AIX, or Linux.

EDN.

Performance Analysis and Applications

PCI Hardware and Software

Adobe Audition CC Classroom in a Book

Symbian OS Internals

ISTCS '92, the Israel Symposium on the Theory of Computing and Systems, came about spontaneously as a result of informal interaction between a group of people who viewed the conference as an appropriate expression of Israeli strength in theoretical aspects of computing and systems. The enthusiasm that the symposium created

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resulted in the submission of a large number of extremely high quality papers, which led in turn to strict acceptance criteria. This volume contains nineteen selected papers representing the cream of Israeli talent in the field, on a variety of active and interesting topics in the theory of computing and systems.

PCI and PCI-X Hardware and Software Architecture and Design
www.digitalguru.com
Symbian OS Internals Real-time Kernel Programming
John Wiley & Sons

Peripheral components range from chip sets for PCI Local Bus and Extended Industry Standard Architecture (EISA) to controllers for cache, Direct Memory Access (DMA), floppy disk and keyboard. This book contains comprehensive charts covering symbols and functions,

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block diagrams and operational and functional descriptions.

THE OPTIMAL DESIGN OF CENTRAL PROCESSOR DATA PATHS

Nuclear Power Plants: Innovative Technologies for Instrumentation and Control Systems

Create Profitable Digital Products for Easy Online Sales

The Fifth International Symposium on Software Reliability, Industrial Safety, Cyber Security and Physical Protection of Nuclear Power Plant (ISNPP)

Dynamic Reconfiguration

This book is a compilation of selected papers from the fifth International Symposium on Software Reliability,

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Industrial Safety, Cyber Security and Physical Protection of Nuclear Power Plant, held in November 2020 in Beijing, China. The purpose of this symposium is to discuss Inspection, test, certification and research for the software and hardware of Instrument and Control (I&C) systems in nuclear power plants (NPP), such as sensors, actuators and control system. It aims to provide a platform of technical exchange and experience sharing for those broad masses of experts and scholars and nuclear power practitioners, and for the combination of production, teaching and research in universities and enterprises to promote the safe development of nuclear power plant. Readers will find a wealth of valuable

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insights into achieving safer and more efficient instrumentation and control systems.

Sustainable energy services to customers - a balanced choice and coordination of energy generated by traditional and alternative sources - are the subject of this new innovative book. The myriad factors involved in modeling an effective sustainable power system are overwhelming. The "Green Islands" project represents a decade of work by over a dozen researchers who have developed a model designed to utilize the potential of distributed clean resources. The key is the proper use of Information Technology (IT). Sited on two islands in the Azores, the project developed the model of careful

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forecasting of demand and supply, down to the minute, coordinating the output of conventional power plants, wind energy, fly wheels, hydroelectricity, demand reduction, and even plug-in electric vehicles to take full advantage of the clean resources available. The energy contingencies of the remote islands are not unique. The issues of integrating promising clean technologies, such as wind, into a complex power grid are challenging in geographically far-flung, island-scale, power systems. Model-based sensing, communications, and decision-making algorithms to coordinate adaptive load management (ALM) could enable customers to utilize just-in-time (JIT), just-in-place (JIP), and just-in-context

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(JIC) energy resources. The distribution of flexible and efficient energy to customers is the goal. The model the authors have developed could change the way power portfolios are built. A new perspective for optimization of green energy is presented in this book. Additional data provided online via Springer represents a repository of real-world electric energy systems and its IT-enabled smarts.

In today's competitive global environment, manufacturers are offered with unprecedented opportunities to build hyper-efficient and highly flexible plants, towards meeting variable market demand, while at the same time supporting new production models such

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as make-to-order (MTO), configure-to-order (CTO) and engineer-to-order (ETO). During the last couple of years, the digital transformation of industrial processes is propelled by the emergence and rise of the fourth industrial revolution (Industry4.0). The latter is based on the extensive deployment of Cyber-Physical Production Systems (CPPS) and Industrial Internet of Things (IIoT) technologies in the manufacturing shopfloor, as well as on the seamless and timely exchange of digital information across supply chain participants. The benefits of Industry 4.0 have been already proven in the scope of pilot and production deployments in a number of different use cases including flexibility in automation,

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predictive maintenance, zero defect manufacturing and more. Despite early implementations and proof-of-concepts, CPPS/IIoT deployments are still in their infancy for a number of reasons, including:

- Manufacturers' poor awareness about digital manufacturing solutions and their business value potential, as well as the lack of relevant internal CPPS/IIoT knowledge.
- The high costs that are associated with the deployment, maintenance and operation of CPPS systems in the manufacturing shopfloors, which are particularly challenging in the case of SME (Small Medium Enterprises) manufacturers that lack the equity capital needed to invest in Industry 4.0.

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The time needed to implement CPPS/IloT and the lack of a smooth and proven migration path from existing OT solutions. □ The uncertainty over the business benefits and impacts of IloT and CPPS technologies, including the lack of proven methods for the techno-economic evaluation of Industry4.0 systems. □ Manufacturers □ increased reliance on external integrators, consultants and vendors. □ The absence of a well-developed value chain needed to sustain the acceptance of these new technologies for digital automation. In order to alleviate these challenges, three European Commission funded projects (namely H2020 FAR-EDGE (<http://www.far-edge.eu/>), H2020 DAEDALUS

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(<http://daedalus.iec61499.eu>) and H2020 AUTOWARE (<http://www.autoware-eu.org/>) have recently joined forces towards a "Digital Shopfloor Alliance". The Alliance aims at providing leading edge and standards based digital automation solutions, along with guidelines and blueprints for their effective deployment, validation and evaluation. The present book provides a comprehensive description of some of the most representative solutions that offered by these three projects, along with the ways these solutions can be combined in order to achieve multiplier effects and maximize the benefits of their use. The presented solutions include standards-based digital automation

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solutions, following different deployment paradigms, such as cloud and edge computing systems. Moreover, they also comprise a rich set of digital simulation solutions, which are explored in conjunction with the H2020 MAYA project (<http://www.maya-euproject.com/>). The latter facilitate the testing and evaluation of what-if scenarios at low risk and cost, but also without disrupting shopfloor operations. As already outlined, beyond leading edge scientific and technological development solutions, the book comprises a rich set of complementary assets that are indispensable to the successful adoption of IIoT/CPPS in the shopfloor. The book is structured in three parts as follows: □ The first

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part of the book is devoted to digital automation platforms. Following an introduction to Industry 4.0 in general and digital automation platforms in particular, this part presents the digital automation platforms of the FAR-EDGE, AUTOWARE and DAEDALUS projects. □ The second part of the book focuses on the presentation of digital simulation and digital twins□ functionalities. These include information about the models that underpin digital twins, as well as the simulators that enable experimentation with these processes over these digital models. □ The third part of the book provides information about complementary assets and supporting services that boost the adoption of digital automation

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functionalities in the Industry4.0 era. Training services, migration services and ecosystem building services are discussed based on the results of the three projects of the Digital Shopfloor Alliance. The target audience of the book includes:

- Researchers in the areas of Digital Manufacturing and more specifically in the areas of digital automation and simulation, who wish to be updated about latest Industry4.0 developments in these areas.
- Manufacturers, with an interest in the next generation of digital automation solutions based on Cyber-Physical systems.
- Practitioners and providers of Industrial IoT solutions, which are interested in the implementation of use cases in automation, simulation

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and supply chain management. Managers wishing to understand technologies and solutions that underpin Industry4.0, along with representative applications in the shopfloor and across the supply chain.

PC Mag

Patents

System on Chip Interfaces for Low Power Design

2017 CFR Annual Print Title 49 Transportation Parts 572 to 999

Containing a Codification of Documents of General Applicability and Future Effect as of December 31, 1948, with Ancillaries and Index

Take a look inside Symbian OS with an under-the-hood view of

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Symbian's revolutionary new real-time smartphone kernel Describes the functioning of the new real-time kernel, which will become ubiquitous on Symbian OS phones in the next 5-10 years Will benefit the base-porting engineer by providing a more solid understanding of the OS being ported Contains an in-depth explanation of how Symbian OS drivers work. Device drivers have changed considerably with the introduction of a single code - this book helps those converting them to the new kernel The book has broad appeal and is relevant to all who work with Symbian OS at a low level, whatever Symbian OS they are targeting Written by the engineers who actually designed and built the real-time kernel

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“The chapter on programming a KMDF hardware driver provides a great example for readers to see a driver being made.” –Patrick Regan, network administrator, Pacific Coast Companies

The First Authoritative Guide to Writing Robust, High-Performance Windows 7 Device Drivers

Windows 7 Device Driver brings together all the information experienced programmers need to build exceptionally reliable, high-performance Windows 7 drivers. Internationally renowned driver development expert Ronald D. Reeves shows how to make the most of Microsoft’s powerful new tools and models; save time and money; and efficiently deliver stable, robust drivers. Drawing on his unsurpassed experience as both a driver developer and instructor, Reeves demystifies Kernel and User Mode Driver development, Windows Driver Foundation (WDF) architecture, driver debugging, and many other key topics. Throughout, he

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provides best practices for all facets of the driver development process, illuminating his insights with proven sample code. Learn how to Use WDF to reduce development time, improve system stability, and enhance serviceability Take full advantage of both the User Mode Driver Framework (UMDF) and the Kernel Mode Driver Framework (KMDF) Implement best practices for designing, developing, and debugging both User Mode and Kernel Mode Drivers Manage I/O requests and queues, self-managed I/O, synchronization, locks, plug-and-play, power management, device enumeration, and more Develop UMDF drivers with COM Secure Kernel Mode Drivers with safe defaults, parameter validation, counted UNICODE strings, and safe device naming techniques Program and troubleshoot WMI support in Kernel Mode Drivers Utilize advanced multiple I/O queuing techniques Whether you're

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creating Windows 7 drivers for laboratory equipment, communications hardware, or any other device or technology, this book will help you build production code more quickly and get to market sooner!

Official Gazette of the United States Patent and Trademark Office

The Tale of Two Low-Cost Green Azores Islands

The Design and Implementation of the FreeBSD Operating System

Hardware and Software, Configuration and Programming, Data

Communication, Operator Control and Monitoring

Digital Design

In the past two decades, breakthroughs in computer technology have made a tremendous impact on optimization. In particular, availability of parallel

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computers has created substantial interest in exploring the use of parallel processing for solving discrete and global optimization problems. The chapters in this volume cover a broad spectrum of recent research in parallel processing of discrete and related problems. The topics discussed include distributed branch-and-bound algorithms, parallel genetic algorithms for large scale discrete problems, simulated annealing, parallel branch-and-bound search under limited-memory constraints, parallelization of greedy randomized adaptive search procedures, parallel optical models of computing, randomized parallel algorithms, general techniques for the design of parallel discrete algorithms, parallel

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algorithms for the solution of quadratic assignment and satisfiability problems. The book will be a valuable source of information to faculty, students and researchers in combinatorial optimization and related areas.

Dynamic Reconfiguration: Architectures and Algorithms offers a comprehensive treatment of dynamically reconfigurable computer architectures and algorithms for them. The coverage is broad starting from fundamental algorithmic techniques, ranging across algorithms for a wide array of problems and applications, to simulations between models. The presentation employs a single reconfigurable model (the reconfigurable mesh) for most

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algorithms, to enable the reader to distill key ideas without the cumbersome details of a myriad of models. In addition to algorithms, the book discusses topics that provide a better understanding of dynamic reconfiguration such as scalability and computational power, and more recent advances such as optical models, run-time reconfiguration (on FPGA and related platforms), and implementing dynamic reconfiguration. The book, featuring many examples and a large set of exercises, is an excellent textbook or reference for a graduate course. It is also a useful reference to researchers and system developers in the area.

The Code of Federal Regulations of the United States of

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America

Parallel Processing of Discrete Problems

Architectures and Algorithms

Logical Partitions on System i5: A Guide to Planning and

Configuring LPAR with HMC on System i

Advanced Industrial Control Technology