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***<http://gateinstructors.in> Solved Papers GATE: Computer Science and Information Technology 10 Years' Solved Papers GATE: Computer Science and Information Technology, a product for The GATE. The book offers the students an opportunity to familiarise themselves with the nature and level of complexity of questions asked in GATE and helps them in topic-wise preparation for the examination. Solutions to most of the questions and answer keys have been provided at the end of each Papers.***

***In the preparation of this book, my aim has been to present the text in a sequential and lucid manner,***

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***containing all essentials of practical surveying. The book proves to be a valuable source of study to those who are preparing for GATE and other competitive examinations. This book contains Nine chapters. The most outstanding feature of the book is the condensation of the exhaustive theory into a systematic, point wise pattern and insertions of explanatory notes particularly with reference to the more common surveying operations for easy learning of the students. A large portion of the material presented in this book has been derived from the work of others . Their contribution is greatly acknowledged. An attempt has been made to also include all the recent developments in the field of surveying.***

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**aspirants to practice and hone their skills required to gain the best score in the examination. It includes more than 25 previous yearsandrsquo; GATE questions segregated topic-wise supported by detailed step-wise solutions for all. Besides, the book presents the exam analysis at the beginning of every unit which will enable a better understanding of the subject. The questions in the chapters are divided according to their marks, hence emphasizing on their importance. This, in turn, will help the students to get an idea about the pattern and weightage of these questions that appeared in the GATE exam every year.**

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single comprehensive reference for the compact models of most commonly used semiconductor devices (both active and passive) represents a significant advantage for the reader. Indeed, several kinds of semiconductor devices are routinely encountered in a single IC design or in a single modeling support group. Compact Modeling includes mostly the material that after several years of IC design applications has been found both theoretically sound and practically significant. Assigning the individual chapters to the groups responsible for the definitive work on the subject assures the highest possible degree of expertise on each of the covered models.

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This book constitutes the refereed proceedings of the 20th International Conference on Integrated Circuit and System Design, PATMOS 2010, held in Grenoble, France, in September 2010. The 24 revised full papers presented and the 9 extended abstracts were carefully reviewed and are organized in topical sections on design flows; circuit techniques; low power circuits; self-timed circuits; process variation; high-level modeling of poweraware heterogeneous designs in SystemC-AMS; and minalogic.

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*propulsors are important for the environmental impact of ships, underwater noise impact on aquatic fauna, and crew and passenger comfort and safety. This book presents nineteen papers devoted to the hydrodynamics of different types of marine propulsors (conventional propellers, thrusters, and novel solutions). Most of the papers are extended papers from the sixth International Symposium on Marine Propulsors (SMP 2019). Several of the papers deal with cavitation, vortices, and energy saving devices. The papers present high-quality research performed using Computational Fluid Dynamics (CFD) and Experimental Fluid Dynamics (EFD) as well Artificial Intelligence (AI).*

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(Second Edition) is helpful for Aspirants of GATE-2022, NET/ARS-2022, SRF-2022 and various Government Competitive Examinations. It contains numerical problem solving approaches. It covers GATE 2007 to 2020 solved question paper. Various competitive exams UNION/STATE PSCs questions also covered in this book. Apart of it, it have model papers for competitive exams for better preparation of Examinations. Pages - 628 Language- English

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Contemporary Library Architecture  
Este libro contiene las presentaciones de la XVII Conferencia de Diseño de Circuitos y Sistemas

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Integrados celebrado en el Palacio de la Magdalena, Santander, en noviembre de 2002. Esta Conferencia ha alcanzado un alto nivel de calidad, como consecuencia de su tradición y madurez, que lo convierte en uno de los acontecimientos más importantes para los circuitos de microelectrónica y la comunidad de diseño de sistemas en el sur de Europa. Desde su origen tiene una gran contribución de Universidades españolas, aunque hoy los autores participan desde catorce países

This book consists of four chapters to address at different modeling levels for different nanoscale



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MOS structures (Single- and Multi-Gate MOSFETs). The collection of these chapters in the book are attempted to provide a comprehensive coverage on the different levels of electrostatics and transport modeling for these devices, and relationships between them. In particular, the issue of quantum transport approaches, analytical predictive 2D/3D modeling and design-oriented compact modeling. It should be of interests to researchers working on modeling at any level, to provide them with a clear explanation of the approaches used and the links with modeling techniques for either higher or lower

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levels. Contents: Monte-Carlo Simulation of Ultra-Thin Film Silicon-on-Insulator MOSFETs (F Gámiz, C Sampedro, L Donetti and A Godoy) Analytical Models and Electrical Characterisation of Advanced MOSFETs in the Quasi-Ballistic Regime (R Clerc and G Ghibaud) Physics Based Analytical Modeling of Nanoscale Multigate MOSFETs (T A Fjeldly and U Monga) Compact Modeling of Double and Tri-Gate MOSFETs (B Iñiguez, R Ritzenthaler and F Lime)

Readership: Scientists, engineers, research leaders, and even investors interested in microelectronics, nanoelectronics, and optoelectronics. It is also

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recommended to graduate students working in these fields. Key Features: This book is part of the Selected Topics in Electronics and Systems edited by Sorin Cristoloveanu (Grenoble INP – Minatec, France) and Michael Shur (Rensselaer Polytechnic Institute, USA). Nanoscale Electron Devices started with a big bang but over the years there are still many challenges unsolved which prevent it from becoming mainstream. This book reignites the interests on research works on different modeling levels for nanoscale semiconductor devices, in particular different nanoscale MOS structures (Single- and

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Multi-Gate MOSFETs)The book is well written, and targeting at graduate students, faculty and researchers working in MOSFETsKeywords:Workshops on Frontiers in Electronics “ WOFE;Carbon Nanotubes;Microelectronics;Nanoelectronics;MOSFETs DCIS2002 Official Gazette of the United States Patent and Trademark Office Model Papers, Question Bank,Practice papers GATE Computer Science and Information Technology

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Programmed Statistics (Question-Answers)  
Advanced Modeling of Nanoscale Electron Devices