

Edk Ii Module Writers Guide

This open access volume provides insight into how organizations change through the adoption of digital technologies. Opportunities and challenges for individuals as well as the organization are addressed. It features four major themes: 1. Current research exploring the theoretical underpinnings of digital transformation of organizations. 2. Insights into available digital technologies as well as organizational requirements for technology adoption. 3. Issues and challenges for designing and implementing digital transformation in learning organizations. 4. Case studies, empirical research findings, and examples from organizations which successfully adopted digital workplace learning.

Revised edition of: *FPGA-based implementation of signal processing systems / Roger Woods ... [et al.]. 2008.*

How to Design and Evaluate Research in Education provides a comprehensive introduction to educational research. Step-by-step analysis of real research studies provides students with practical examples of how to prepare their work and read that of others. End-of-chapter problem sheets, comprehensive coverage of data analysis, and information on how to prepare research proposals and reports make it appropriate both for courses that focus on doing research and for those that stress how to read and understand research.

The definitive reference on electromagnetic shielding materials, configurations, approaches, and analyses This reference provides a comprehensive survey of options for the reduction of the electromagnetic field levels in prescribed areas. After an introduction and an overview of available materials, it discusses figures of merit for shielding configurations, the shielding effectiveness of stratified media, numerical methods for shielding analyses, apertures in planar metal screens, enclosures, and cable shielding. Up to date and comprehensive, Electromagnetic Shielding: Explores new and innovative techniques in electromagnetic shielding Presents a critical approach to electromagnetic shielding that highlights the limits of formulations based on plane-wave sources Analyzes aspects not normally considered in electromagnetic shielding, such as the effects of the content of the shielding enclosures Includes references at the end of each chapter to facilitate further study The last three chapters discuss frequency-selective shielding, shielding design procedures, and uncommon ways of shielding-areas ripe for further research. This is an authoritative, hands-on resource for practicing telecommunications and electrical engineers, as well as researchers in industry and academia who are involved in the design and analysis of electromagnetic shielding structures.

Building Secure Firmware
Intel Galileo and Intel Galileo Gen 2
Reproductive, Maternal, Newborn, and Child Health
Designing Connected, Pervasive, Media-rich Systems
PC Magazine
Embedded Systems Design with Platform FPGAs
Resources in Education

Master the booting procedure of various operating systems with in-depth analysis of bootloaders and firmware. The primary focus is on the Linux booting procedure along with other popular operating systems such as Windows and Unix. Hands-on Booting begins by explaining what a bootloader is, starting with the Linux bootloader followed by bootloaders for Windows and Unix systems. Next, you'll address the BIOS and UEFI firmware by installing multiple operating systems on one machine and booting them through the Linux bootloader. Further, you'll see the kernel's role in the booting procedure of the operating system and the dependency between kernel, initramfs, and dracut. You'll also cover systemd, examining its structure and how it mounts the user root filesystem. In the final section, the book explains troubleshooting methodologies such as debugging shells followed by live images and rescue mode. On completing this book, you will understand the booting process of major operating systems such as Linux, Windows, and Unix. You will also know how to fix the Linux booting issues through various boot modes. What You Will Learn Examine the BIOS and UEFI firmware Understanding the Linux boot loader (GRUB)Work with initramfs, dracut, and systemdFix can't-boot issues on Linux Who This Book Is For Linux users, administrators, and developers.

This presents practical techniques for interpolation and estimation problems when analysing data from field observations.

The book covers various aspects of VHDL programming and FPGA interfacing with examples and sample codes giving an overview of VLSI technology, digital circuits design with VHDL, programming, components, functions and procedures, and arithmetic designs followed by coverage of the core of external I/O programming, algorithmic state machine based system design, and real-world interfacing examples.

This work is a comprehensive study of the field. It provides an entry point to the novice willing to move in the research field reconfigurable computing, FPGA and system on programmable chip design. The book can also be used as teaching reference for a graduate course in computer engineering, or as reference to advance electrical and computer engineers. It provides a very strong theoretical and practical background to the field, from the early Estrin's machine to the very modern architecture such as embedded logic devices.

Successful IoT Device/Edge and Platform Security Deployment
Embedded Firmware Solutions
Partial Evaluation and Automatic Program Generation
Secure Coding
FPGA-Based Embedded System Developer's Guide
A Guide for Embedded Firmware Developers, 2nd Edition
Principles and Practices

Mastering the language of schooling is essential for learners to develop the skills necessary for school success and for critical thinking. It is fundamental for participation in democratic societies, and for social inclusion and cohesion. This handbook is a policy convergence and coherence between the linguistic dimensions of various school subjects. It proposes measures to make explicit – in curricula, pedagogic material and teacher training – the specific linguistic norms and competences which learners must master in learning modalities that should allow all learners, and in particular the most vulnerable among them, to benefit from diversified language-learning situations in order to develop their cognitive and linguistic capacities.

Intel® Galileo and Intel® Galileo Gen 2: API Features and Arduino Projects for Linux Programmers provides detailed information about Intel® Galileo and Intel® Galileo Gen 2 boards for all software developers interested in Arduino and the Linux platform. The book is an introduction for developers on natively using Linux. Author Manoel Carlos Ramon is a member of the Intel Galileo development team; in this book he draws on his practical experience in working on the Galileo project as he shares the team's findings, projects, and experiences with the open source community. His areas of expertise are wide-ranging, including Linux-embedded kernel and device drivers, C/C++, Java, OpenGL, Assembler, Android NDK/SDK/ADK, and 2G/3G/4G modem integration. He has more than 17 years of experience with mobile devices and embedded circuits. His personal blog about programming is BytesThink (www.bytesthink.com).

FPGAs have almost entirely replaced the traditional Application Specific Standard Parts (ASSP) such as the 74xx logic chip families because of their superior size, versatility, and speed. For example, FPGAs provide over a million fold increase in gates compared to ASSPs. Designing for hands-on exercises has relied on ASSP parts, primarily because of their simplicity and ease of use for the novice. Not only is this approach technically outdated, but it also severely limits the complexity of the designs that can be implemented. By introducing readers to FPGAs, the author familiarized with current digital technology and the skills to implement complex, sophisticated designs. However, working with FPGAs comes at a cost of increased complexity, notably the mastering of an HDL language, such as Verilog. Therefore, this book starts with basic digital design concepts and then applies them through exercises; second, it implements these digital designs by teaching the user the syntax of the Verilog language while implementing the exercises. Finally, it employs contemporary digital hardware, such as the Zynq SoC, as a basic music player, a frequency and period counter and it ends with a microprocessor being embedded in the fabric of the FPGA to communicate with the PC. In the process, readers learn about digital mathematics and digital-to-analog converter concepts. This book is about the Zynq-7000 All Programmable System on Chip, the family of devices from Xilinx that combines an application-grade ARM Cortex-A9 processor with traditional FPGA logic fabric. Catering for both new and experienced readers, it covers first starting with a clear overview of the device architecture, and an introduction to the design tools and processes for developing a Zynq SoC. Later chapters progress to more advanced topics such as embedded systems development, IP block design and operation. From the perspective, the book also compares Zynq with other device alternatives, and considers end-user applications. The Zynq Book is accompanied by a set of practical tutorials hosted on a companion website. These tutorials will guide the reader through first step-by-step based embedded systems design.

How to Avoid Security Problems the Right Way
 100 Power Tips for FPGA Designers
 Rootkits and Bootkits
 Quick Boot
 Choices for Quality
 The Independent Guide to IBM-standard Personal Computing
 Introduction to Reconfigurable Computing

Modern embedded systems are used for connected, media-rich, and highly integrated handheld devices such as mobile phones, digital cameras, and MP3 players. All of these embedded systems require networking, graphic user interfaces, and are opposed to traditional embedded processors that can perform only limited functions for industrial applications. While most books focus on these controllers, Modern Embedded Computing provides a thorough understanding of the platform and the design of embedded computing systems that drive mobile devices. The book offers a comprehensive view of developing a framework for embedded systems-on-chips. Examples feature the Intel Atom processor, which is used in high-end mobile devices, Internet-enabled TVs, tablets, and net books. Beginning with a discussion of embedded platform architecture and Intel Atom-specific architecture, modular chapters cover system boot-up, operating systems, power optimization, graphics and connectivity, and platform tuning. Companion lab materials compliment the chapters, offering hands-on embedded design experience. Learn embedded systems design with the Intel Atom Processor, based on the dominant PC chip architecture, and offer comparisons to other platforms Design embedded processors for systems that support gaming, in-vehicle infotainment, medical records retrieval, point-of-sale purchasing, networking, digital storage, and many more retail, consumer, and industrial applications Explore companion lab materials online that offer hands-on embedded design experience

"... an engaging book that will empower readers in both large and small software development and engineering organizations to build security into their products. ... Readers are armed with firm solutions for the fight against cyber threats." —Dr. Larry Ponemon, Ponemon Institute "... the definitive handbook for security professionals. Dr. Ransome, Anmol Misra, and Brook Schoenfeld deftly outline the procedures and policies needed to integrate real security into the software development process. ...A must-have for anyone on the front lines of the security profession. Leighton, Colonel, USAF (Ret.), Cedric Leighton Associates "Dr. Ransome, Anmol Misra, and Brook Schoenfeld give you a magic formula in this book - the methodology and process to build security into the entire software development life cycle. ... is secured at the source!" —Eric S. Yuan, Zoom Video Communications There is much publicity regarding network security, but the real cyber Achilles' heel is insecure software. Millions of software vulnerabilities create a cyber house of cards for our digital lives. In response, security people build ever more elaborate cyber fortresses to protect this vulnerable software. Despite their efforts, cyber fortifications consistently fail to protect our digital treasures. Why? The security industry has failed with the creative, innovative people who write software. Core Software Security expounds developer-centric software security, a holistic process to engage creativity for security. As long as software is developed by humans, it requires that the Developer-centric security is not only feasible but also cost effective and operationally relevant. The methodology builds security into software development, which lies at the heart of our cyber infrastructure. Whatever development methodology is used, security should be secured at the source. Book Highlights: Supplies a practitioner's view of the SDL Considers Agile as a security enabler Covers the privacy elements in an SDL Outlines a holistic business-savvy SDL framework that includes people, processes, and metrics Highlights the key success factors, deliverables, and metrics for each phase of the SDL Examines cost efficiencies, optimized performance, and organizational structure of a developer-centric software security program and PSIRT Includes a checklist to architect Brook Schoenfeld who shares his insights and experiences in applying the book's SDL framework View the authors' website at http://www.androidinsecurity.com/

This book constitutes the refereed post-proceedings of the Second International Conference on High Performance Networking, Computing, and Communication systems, ICHCC 2011, held in Singapore in May 2011. The conference was held in conjunction with the International Conference on Theoretical and Mathematical Foundations of Computer Science, ICTMF 2011, which proceedings are published in CCIS 164. The 84 revised selected papers presented were carefully reviewed and selected for inclusion in this volume. The topics covered range from computational science, engineering and technology to digital signal processing, and computational biology to game theory, and other related topics.

Most organizations have a firewall, antivirus software, and intrusion detection systems, all of which are intended to keep attackers out. So why is computer security a bigger problem today than ever before? The answer is simple--bad software. Computer security problems. Traditional solutions simply treat the symptoms, not the problem, and usually do so in a reactive way. This book teaches you how to take a proactive approach to computer security. Building Secure Software covers the methodology and process to build security into the software development process. ...A must-have for anyone who have come to realize that software is the product of people who intend to make their code behave. Written for anyone involved in software development and use—from managers to coders—this book is your first step toward building more secure software. Building Secure Software provides expert advice on how to help you ensure the security of essential software. If you consider threats and vulnerabilities early in the development cycle you can build security into your system. With this book you will learn how to determine an acceptable level of risk and how to plug security holes before software is even shipped. Inside you'll find the ten guiding principles for software security, as well as detailed coverage of: Software risk management for security Selecting technologies to make your code more secure Software security in open source and proprietary software How to audit software The dreaded buffer overflow Access control and password authentication Random number generation Applying cryptography Trust management and input Client-side security Designing secure software by building secure software can you defend yourself against security breaches and gain the confidence that comes with knowing you won't have to play the "penetrate and patch" game anymore. Get it right the first time. Let these experts help you properly design your system: save time, money, and credibility; and preserve your customers' trust.

FPGA-based Implementation of Signal Processing Systems
 A handbook for curriculum development and teacher training
 Introduction to Geostatistics
 Disease Control Priorities, Third Edition (Volume 2)
 How to Design and Evaluate Research in Education
 Companion volume
 Modern Embedded Computing

Rootkits and Bootkits will teach you how to understand and counter sophisticated, advanced threats buried deep in a machine's boot process or UEFI firmware. With the aid of numerous case studies and professional research from three of the world's leading security experts, you'll trace malware development over time from rootkits like TDL3 to present-day UEFI implants and examine how they infect a system, persist through reboot, and evade security software. As you inspect and dissect real malware, you'll learn:

- How Windows boots—including 32-bit, 64-bit, and UEFI mode—and where to find vulnerabilities
- The details of boot process security mechanisms like Secure Boot, including an overview of Virtual Secure Mode (VSM) and Device Guard
- Reverse engineering and forensic techniques for analyzing real malware, including bootkits like Rovnix/Carberp, Gapz, TDL4, and the infamous rootkits TDL3 and Festi
- How to perform static and dynamic analysis using emulation and tools like Bochs and IDA Pro
- How to better understand the delivery stage of threats against BIOS and UEFI firmware in order to create detection capabilities
- How to use virtualization tools like VMware Workstation to reverse engineer bootkits and the Intel Chipsec tool to dig into forensic analysis

Cybercrime syndicates and malicious actors will continue to write ever more persistent and covert attacks, but the game is not lost.

Explore the cutting edge of malware analysis with Rootkits and Bootkits. Covers boot processes for Windows 32-bit and 64-bit operating systems.

The evaluation of reproductive, maternal, newborn, and child health (RMNCH) by the Disease Control Priorities, Third Edition (DCP3) focuses on maternal conditions, childhood illness, and malnutrition. Specifically, the chapters address acute illness and undernutrition in children, principally under age 5. It also covers maternal mortality, morbidity, stillbirth, and influences to pregnancy and pre-pregnancy. Volume 3 focuses on developments since the publication of DCP2 and will also include the transition to older childhood, in particular, the overlap and commonality with the child development volume. The DCP3 evaluation of these conditions produced three key findings: 1. There is significant difficulty in measuring the burden of key conditions such as unintended pregnancy, unsafe abortion, nonsexually transmitted infections, infertility, and violence against women. 2. Investments in the continuum of care can have significant returns for improved and equitable access, health, poverty, and health systems. 3. There is a large difference in how RMNCH conditions affect different income groups; investments in RMNCH can lessen the disparity in terms of both health and financial risk.

High Performance Networking, Computing, and Communication SystemsSecond International Conference ICHCC 2011, Singapore, May 5-6, 2011, Selected PapersSpringer

The CEFR Companion volume broadens the scope of language education. It reflects academic and societal developments since the publication of the Common European Framework of Reference for Languages (CEFR) and updates the 2001 version. It owes much to the contributions of members of the language teaching profession across Europe and beyond. This volume contains: ► an explanation of the key aspects of the CEFR for teaching and learning; ► a complete set of updated CEFR descriptors that replaces the 2001 set with: - modality-inclusive and gender-neutral descriptors; - added detail on listening and reading; - a new Pre-A1 level, plus enriched description at A1 and C levels; - a replacement scale for phonological competence; - new scales for mediation, online interaction and plurilingual/pluricultural competence; - new scales for sign language competence; ► a short report on the four-year development, validation and consultation processes. The CEFR Companion volume represents another step in a process of engagement with language education that has been pursued by the Council of Europe since 1971 and which seeks to: ► promote and support the learning and teaching of modern languages; ► enhance intercultural dialogue, and thus mutual understanding, social cohesion and democracy; ► protect linguistic and cultural diversity in Europe; and ► promote the right to quality education for all.

Applied Digital Logic Exercises Using FPGAs
Demystifying Internet of Things Security
Development Best Practices for the Internet of Things
PC/Computing
Linux Dictionary
Debugging with GDB
The Zynq Book

Germany's economic miracle is a widely-known phenomenon, and the world-leading, innovative products and services associated with German companies are something that others seek to imitate. In The 'Made in Germany'Á Champion Brands, Ugesh A. Joseph provides an extensively researched, insightful look at over 200 of Germany's best brands to see what they stand for, what has made them what they are today, and what might be transferable. The way Germany is branded as a nation carries across into the branding of its companies and services, particularly the global superstar brands - truly world-class in size, performance and reputation. Just as important are the medium-sized and small enterprises, known as the 'Mittelstand'. These innovative and successful enterprises from a wide range of industries and product / service categories are amongst the World market leaders in their own niche and play a huge part in making Germany what it is today. The book also focuses on German industrial entrepreneurship and a selection of innovative and emergent stars. All these companies are supported and encouraged by a sophisticated infrastructure of facilitators, influencers and enhancers - the research, industry, trade and standards organizations, the fairs and exhibitions and all the social and cultural factors that influence, enhance and add positive value to the country's image. Professionals or academics interested in business; entrepreneurship; branding and marketing; product or service development; international trade and business development policy, will find fascinating insights in this book; while those with an interest in Germany from emerging industrial economies will learn something of the secrets of German success.

Quick Boot is designed to give developers a background in the basic architecture and details of a typical boot sequence. More specifically, this book describes the basic initialization sequence that allows developers the freedom to boot an OS

without a fully featured system BIOS. Various specifications provide the basics of both the code bases and the standards. This book also provides insights into optimization techniques for more advanced developers. With proper background information, the required specifications on hand, and diligence, many developers can create quality boot solutions using this text. Pete Dice is Engineering Director of Verifone, where he manages OS Engineering teams in Dublin, Ireland and Riga Latvia. Dice successfully launched Intel(R) Quark(TM), Intel's first generation SoC as well as invented the Intel(R) Galileo(TM) development board and developed a freemium SW strategy to scale Intel IoT gateway features across product lines. He is also credited with architecting the "Moon Island" software stack and business model.

Use this book to build secure firmware. As operating systems and hypervisors have become successively more hardened, malware has moved further down the stack and into firmware. Firmware represents the boundary between hardware and software, and given its persistence, mutability, and opaqueness to today's antivirus scanning technology, it represents an interesting target for attackers. As platforms are universally network-connected and can contain multiple devices with firmware, and a global supply chain feeds into platform firmware, assurance is critical for consumers, IT enterprises, and governments. This importance is highlighted by emergent requirements such as NIST SP800-193 for firmware resilience and NIST SP800-155 for firmware measurement. This book covers the secure implementation of various aspects of firmware, including standards-based firmware—such as support of the Trusted Computing Group (TCG), Desktop Management Task Force (DMTF), and Unified Extensible Firmware Interface (UEFI) specifications—and also provides code samples and use cases. Beyond the standards, alternate firmware implementations such as ARM Trusted Firmware and other device firmware implementations (such as platform roots of trust), are covered. What You Will learn Get an overview of proactive security development for firmware, including firmware threat modeling Understand the details of architecture, including protection, detection, recovery, integrity measurement, and access control Be familiar with best practices for secure firmware development, including trusted execution environments, cryptography, and language-based defenses Know the techniques used for security validation and maintenance Who This Book Is For Given the complexity of modern platform boot requirements and the threat landscape, this book is relevant for readers spanning from IT decision makers to developers building firmware

Embedded Firmware Solutions is the perfect introduction and daily-use field guide--for the thousands of firmware designers, hardware engineers, architects, managers, and developers--to Intel's new firmware direction (including Quark coverage), showing how to integrate Intel® Architecture designs into their plans. Featuring hands-on examples and exercises using Open Source codebases, like Coreboot and EFI Development Kit (tianocore) and Chromebook, this is the first book that combines a timely and thorough overview of firmware solutions for the rapidly evolving embedded ecosystem with in-depth coverage of requirements and optimization.

Learn the Boot Process of Linux, Windows, and Unix

Reversing Modern Malware and Next Generation Threats

Core Software Security

The GNU Source-level Debugger

High Performance Networking, Computing, and Communication Systems

Electromagnetic Shielding

Embedded Processing with the Arm Cortex-A9 on the Xilinx Zynq-7000 All Programmable Soc

How to build low-cost, royalty-free embedded solutions with eCos, covers ecos architecture, installation, configuration, coding, debugging, bootstrapping, porting, and more, includes open source tools on CD-ROM for a complete embedded software development environment with eCos as the core.

This book provides an overview of modern boot firmware, including the Unified Extensible Firmware Interface (UEFI) and its associated EFI Developer Kit II (EDKII) firmware. The authors have each made significant contributions to developments in these areas. The reader will learn to use the latest developments in UEFI on modern hardware, including open source firmware and open hardware designs. The book begins with an exploration of interfaces exposed to higher-level software and operating systems, and commences to the left of the boot timeline, describing the flow of typical systems, beginning with the machine restart event. Software engineers working with UEFI will benefit greatly from this book, while specific sections of the book address topics relevant for a general audience: system architects, pre-operating-system application developers, operating system vendors (loader, kernel), independent hardware vendors (such as for plug-in adapters), and developers of end-user applications. As a secondary audience, project technical leaders or managers may be interested in this book to get a feel for what their engineers are doing. The reader will find: An overview of UEFI and underlying Platform Initialization (PI) specifications How to create UEFI applications and drivers Workflow to design the firmware solution for a modern platform Advanced usages of UEFI firmware for security and manageability

Break down the misconceptions of the Internet of Things by examining the different security building blocks available in Intel Architecture (IA) based IoT platforms. This open access book reviews the threat pyramid, secure boot, chain of trust, and the SW stack leading up to defense-in-depth. The IoT presents unique challenges in implementing security and Intel has both CPU and Isolated Security Engine capabilities to simplify it. This book explores the challenges to secure these devices to make them immune to different threats originating from within and outside the network. The requirements and robustness rules to protect the assets vary greatly and there is no single blanket solution approach to implement security. Demystifying Internet of Things Security provides clarity to industry professionals and provides an overview of different security solutions What You'll Learn Secure devices, immunizing them against different threats originating from inside and outside the network Gather an overview of the different security building blocks available in Intel Architecture (IA) based IoT platforms Understand the threat pyramid, secure boot, chain of trust, and the software stack leading up to defense-in-depth Who This Book Is For Strategists, developers, architects, and managers in the embedded and Internet of Things (IoT) space trying to understand and implement the security in the IoT devices/platforms.

Lists citations with abstracts for aerospace related reports obtained from world wide sources and announces documents that have recently been entered into the NASA Scientific and Technical Information Database.

Hands-on Booting

Common European Framework of Reference for Languages: Learning, Teaching, assessment

The 'Made in Germany' Champion Brands

EdPsych Modules

Architectures, Algorithms, and Applications

24 Deadly Sins of Software Security: Programming Flaws and How to Fix Them

Now with SAGE Publications, Cheryl Cisero Durwin and Marla Reese-Weber 's EdPsych Modules uses an innovative implementation of case studies and a modular format to address the challenge of effectively connecting theory and research to practice.

Each module is a succinct, stand-alone topic that represents every subject found in traditional chapter texts and can be used in any order for maximum flexibility in organizing your course. Each of the book 's eight units of modules begins with a set of four case studies—early childhood, elementary, middle school, and secondary—and ends with "Assess" and "Reflect and Evaluate" questions and activities to encourage comprehension and application of the research and theories presented. The case approach and the extensive pedagogy that support it allows students to constantly see the applications of the theories and research that they are studying in the text.

Despite their myriad manifestations and different targets, nearly all attacks on computer systems have one fundamental cause: the code used to run far too many systems today is not secure. Flaws in its design, implementation, testing, and operations allow attackers all-too-easy access. "Secure Coding, by Mark G. Graff and Ken vanWyk, looks at the problem of bad code in a new way. Packed with advice based on the authors' decades of experience in the computer security field, this concise and highly readable book explains why so much code today is filled with vulnerabilities, and tells readers what they must do to avoid writing code that can be exploited by attackers. Beyond the technical, "Secure Coding sheds new light on the economic, psychological, and sheer practical reasons why security vulnerabilities are so ubiquitous today. It presents a new way of thinking about these vulnerabilities and ways that developers can compensate for the factors that have produced such unsecured software in the past. It issues a challenge to all those concerned about computer security to finally make a commitment to building code the right way.

"What makes this book so important is that it reflects the experiences of two of the industry's most experienced hands at getting real-world engineers to understand just what they're being asked for when they're asked to write secure code. The book reflects Michael Howard's and David LeBlanc's experience in the trenches working with developers years after code was long since shipped, informing them of problems." --From the Foreword by Dan Kaminsky, Director of Penetration Testing, IOActive Eradicate the Most Notorious Insecure Designs and Coding Vulnerabilities Fully updated to cover the latest security issues, 24 Deadly Sins of Software Security reveals the most common design and coding errors and explains how to fix each one-or better yet, avoid them from the start. Michael Howard and David LeBlanc, who teach Microsoft employees and the world how to secure code, have partnered again with John Viega, who uncovered the original 19 deadly programming sins. They have completely revised the book to address the most recent vulnerabilities and have added five brand-new sins. This practical guide covers all platforms, languages, and types of applications. Eliminate these security flaws from your code: SQL injection Web server- and client-related vulnerabilities Use of magic URLs, predictable cookies, and hidden form fields Buffer overruns Format string problems Integer overflows C++ catastrophes Insecure exception handling Command injection Failure to handle errors Information leakage Race conditions Poor usability Not updating easily Executing code with too much privilege Failure to protect stored data Insecure mobile code Use of weak password-based systems Weak random numbers Using cryptography incorrectly Failing to protect network traffic Improper use of PKI Trusting network name resolution

Embedded Systems Design with Platform FPGAs introduces professional engineers and students alike to system development using Platform FPGAs. The focus is on embedded systems but it also serves as a general guide to building custom computing systems. The text describes the fundamental technology in terms of hardware, software, and a set of principles to guide the development of Platform FPGA systems. The goal is to show how to systematically and creatively apply these principles to the construction of application-specific embedded system architectures. There is a strong focus on using free and open source software to increase productivity. Each chapter is organized into two parts. The white pages describe concepts, principles, and general knowledge. The gray pages provide a technical rendition of the main issues of the chapter and show the concepts applied in practice. This includes step-by-step details for a specific development board and tool chain so that the reader can carry out the same steps on their own. Rather than try to demonstrate the concepts on a broad set of tools and boards, the text uses a single set of tools (Xilinx Platform Studio, Linux, and GNU) throughout and uses a single developer board (Xilinx ML-510) for the examples. Explains how to use the Platform FPGA to meet complex design requirements and improve product performance Presents both fundamental concepts together with pragmatic, step-by-step instructions for building a system on a Platform FPGA

Includes detailed case studies, extended real-world examples, and lab exercises

API Features and Arduino Projects for Linux Programmers

Embedded Software Development with ECos

Learning Well at Work

Armoring the Foundation of the Platform

Power System Dynamics and Stability

Scientific and Technical Aerospace Reports

Security at the Source

Explores the principles of automatic partial evaluation, provides simple and complete algorithms, and demonstrates via examples that specialization can increase efficiency. Covers partial evaluation of programming languages from C and Prolog to Scheme and the lambda calculus. For researchers, programmers, and students in advanced programming languages.

This document is designed to be a resource for those Linux users wishing to seek clarification on Linux/UNIX/POSIX related terms and jargon. At approximately 24000 definitions and two thousand pages it is one of the largest Linux related dictionaries currently available. Due to the rapid rate at which new terms are being created it has been decided that this will be an active project. We welcome input into the content of this document. At this moment in time half yearly updates are being envisaged. Please note that if you wish to find a 'Computer Dictionary' then see the 'Computer Dictionary Project' at <http://computerdictionary.tsf.org.za/> Searchable databases exist at locations such as: <http://www.swpearl.com/eng/scripts/dictionary/> (SWP) Sun Wah-PearL Linux Training and Development Centre is a centre of the Hong Kong Polytechnic University, established in 2000. Presently SWP is delivering professional grade Linux and related Open Source Software (OSS) technology training and consultant service in Hong Kong. SWP has an ambitious aim to promote the use of Linux and related Open Source Software (OSS) and Standards. The vendor independent positioning of SWP has been very well perceived by the market.

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