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PLEASE PROVIDE DESCRIPTION

This book constitutes the refereed proceedings of the 6th International Workshop on Post-Quantum Cryptography, PQCrypto 2014, held in Waterloo, ON, Canada, in October 2014. The 16 revised full papers presented were carefully

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reviewed and selected from 37 submissions. The papers cover all technical aspects of cryptographic research related to the future world with large quantum computers such as code-based cryptography, lattice-based cryptography, multivariate cryptography, isogeny-based cryptography, security proof frameworks, cryptanalysis and implementations.

Provides information on writing a

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driver in Linux, covering such topics as character devices, network interfaces, driver debugging, concurrency, and interrupts.

The Art of Hardware Architecture
International Conference, ISCTCS 2013,
Beijing, China, November 2013, Revised
Selected Papers

Internet of Things, Smart Spaces, and
Next Generation Networks and Systems
Sound - Perception - Performance
Designing with Xilinx® FPGAs

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Musical Performance covers many aspects like Musical Acoustics, Music Psychology, or motor and prosodic actions. It deals with basic concepts of the origin of music and its evolution, ranges over neurocognitive foundations, and covers computational, technological, or simulation solutions. This volume gives an overview about current research in the foundation of musical performance studies on all these levels. Recent concepts of synchronized systems, evolutionary concepts, basic understanding of performance as Gestalt patterns, theories of skill as performance goals or historical aspects are covered. The

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neurocognitive basis of motor action in terms of music, musical syntax, as well as therapeutic aspects are discussed. State-of-the-art applications in performance realizations, like virtual room acoustics, virtual musicians, new concepts of real-time physical modeling using complex performance data as input or sensor and gesture studies with soft- and hardware solutions are presented. So although the field is still much larger, this volume presents current trends in terms of understanding, implementing, and perceiving performance.

The essential introduction to the principles and

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applications of feedback systems—now fully revised and expanded This textbook covers the mathematics needed to model, analyze, and design feedback systems. Now more user-friendly than ever, this revised and expanded edition of Feedback Systems is a one-volume resource for students and researchers in mathematics and engineering. It has applications across a range of disciplines that utilize feedback in physical, biological, information, and economic systems. Karl Åström and Richard Murray use techniques from physics, computer science, and operations research to introduce control-oriented modeling.

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They begin with state space tools for analysis and design, including stability of solutions, Lyapunov functions, reachability, state feedback observability, and estimators. The matrix exponential plays a central role in the analysis of linear control systems, allowing a concise development of many of the key concepts for this class of models. Åström and Murray then develop and explain tools in the frequency domain, including transfer functions, Nyquist analysis, PID control, frequency domain design, and robustness. Features a new chapter on design principles and tools, illustrating the types of

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**problems that can be solved using feedback
Includes a new chapter on fundamental limits
and new material on the Routh-Hurwitz criterion
and root locus plots Provides exercises at the end
of every chapter Comes with an electronic
solutions manual An ideal textbook for
undergraduate and graduate students
Indispensable for researchers seeking a self-
contained resource on control theory
This book constitutes the joint refereed
proceedings of the 15th International Conference
on Next Generation Wired/Wireless Advanced
Networks and Systems, NEW2AN 2015, and the**

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8th Conference on Internet of Things and Smart Spaces, ruSMART 2015, held in St. Petersburg, Russia, in August 2015. The 74 revised full papers were carefully reviewed and selected from numerous submissions. The 15 papers selected for ruSMART are organized in topical sections on IoT infrastructure, IoT platforms, smart spaces and IoT cases, and smart services and solutions. The 59 papers from NEW2AN deal with the following topics: streaming, video, and TCP applications, mobile "ad hoc" networks, security, and clouds, sensor networks and IoT, cellular systems, novel systems and techniques, business

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and services, signals and circuits, optical and satellite systems, and advanced materials and their properties.

Introduction to Embedded Systems, Second Edition

15th International Conference, NEW2AN 2015, and 8th Conference, ruSMART 2015, St.

Petersburg, Russia, August 26-28, 2015, Proceedings

Advanced Signal Integrity for High-Speed Digital Designs

Maximum PC

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Embedded Systems Design with Platform FPGAs introduces professional engineers and students alike to system development using Platform FPGAs. The focus is on embedded systems but it also serves as a general guide to building custom computing systems. The text describes the fundamental technology in terms of hardware, software, and a set of principles to guide the development of Platform FPGA systems. The goal is to show how to systematically and creatively apply these principles to the construction of application-specific embedded system architectures. There is a strong focus on using free and open source software to increase productivity. Each chapter is organized into two parts. The white pages describe concepts,

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principles, and general knowledge. The gray pages provide a technical rendition of the main issues of the chapter and show the concepts applied in practice. This includes step-by-step details for a specific development board and tool chain so that the reader can carry out the same steps on their own. Rather than try to demonstrate the concepts on a broad set of tools and boards, the text uses a single set of tools (Xilinx Platform Studio, Linux, and GNU) throughout and uses a single developer board (Xilinx ML-510) for the examples. Explains how to use the Platform FPGA to meet complex design requirements and improve product performance Presents both fundamental concepts together with pragmatic, step-by-step

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instructions for building a system on a Platform FPGA Includes detailed case studies, extended real-world examples, and lab exercises

- PCI EXPRESS is considered to be the most general purpose bus so it should appeal to a wide audience in this arena.
- Today's buses are becoming more specialized to meet the needs of the particular system applications, building the need for this book.
- Mindshare and their only competitor in this space, Solari, team up in this new book.

An introduction to the engineering principles of embedded systems, with a focus on modeling, design, and analysis of cyber-physical systems. The most visible use of computers and software is processing

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information for human consumption. The vast majority of computers in use, however, are much less visible. They run the engine, brakes, seatbelts, airbag, and audio system in your car. They digitally encode your voice and construct a radio signal to send it from your cell phone to a base station. They command robots on a factory floor, power generation in a power plant, processes in a chemical plant, and traffic lights in a city. These less visible computers are called embedded systems, and the software they run is called embedded software. The principal challenges in designing and analyzing embedded systems stem from their interaction with physical processes. This book takes a cyber-physical approach to embedded systems,

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introducing the engineering concepts underlying embedded systems as a technology and as a subject of study. The focus is on modeling, design, and analysis of cyber-physical systems, which integrate computation, networking, and physical processes. The second edition offers two new chapters, several new exercises, and other improvements. The book can be used as a textbook at the advanced undergraduate or introductory graduate level and as a professional reference for practicing engineers and computer scientists. Readers should have some familiarity with machine structures, computer programming, basic discrete mathematics and algorithms, and signals and systems.

A Cyber-Physical Systems Approach

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Building Embedded Linux Systems

Signal Integrity for PCB Designers

Concepts, Techniques, Tricks, and Traps

Design Implications for Hardware and Software
Developers

*Explains how to upgrade and repair
processors, memory, connections,
drives, multimedia cards, and
peripherals.*

*PCMag.com is a leading authority on
technology, delivering Labs-based,
independent reviews of the latest*

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products and services. Our expert industry analysis and practical solutions help you make better buying decisions and get more from technology. Maximum PC is the magazine that every computer fanatic, PC gamer or content creator must read. Each and every issue is packed with punishing product reviews, insightful and innovative how-to stories and the illuminating technical articles that enthusiasts crave.

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The Journal for UNIX System

Administrators

A Wireless Technology Revolution

SAS Storage Architecture

PC Mag

Post-Quantum Cryptography

Singapore's leading tech magazine gives its readers the power to decide with its informative articles and in-depth reviews.

There's a great deal of excitement surrounding the use of Linux in embedded systems -- for everything from cell phones to car ABS systems and water-

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filtration plants -- but not a lot of practical information. Building Embedded Linux Systems offers an in-depth, hard-core guide to putting together embedded systems based on Linux. Updated for the latest version of the Linux kernel, this new edition gives you the basics of building embedded Linux systems, along with the configuration, setup, and use of more than 40 different open source and free software packages in common use. The book also looks at the strengths and weaknesses of using Linux in an embedded system, plus a discussion of licensing issues, and an introduction to real-time, with a discussion of real-

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time options for Linux. This indispensable book features arcane and previously undocumented procedures for: Building your own GNU development toolchain Using an efficient embedded development framework Selecting, configuring, building, and installing a target-specific kernel Creating a complete target root filesystem Setting up, manipulating, and using solid-state storage devices Installing and configuring a bootloader for the target Cross-compiling a slew of utilities and packages Debugging your embedded system using a plethora of tools and techniques Using the uClibc, BusyBox, U-Boot, OpenSSH, thttpd, tftp, strace, and

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gdb packages By presenting how to build the operating system components from pristine sources and how to find more documentation or help, Building Embedded Linux Systems greatly simplifies the task of keeping complete control over your embedded operating system.

PCI-X System Architecture is a detailed and comprehensive guide to the PCI-X technology. It highlights the many changes and improvements from PCI 2.2 to PCI-X, so that you can build on your PCI knowledge to master PCI-X with greater ease. The book discusses the drawbacks of PCI and how PCI-X solves these problems, achieving faster

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transfer rates. In addition, it presents in-depth information and practical guidance on the PCI-X transaction protocol, device configuration for PCI-X, load tuning, PCI-X bridges, error detection and handling, and electrical issues.

WiMAX

Complete Computer Hardware Only

Fix Your Own PC

Embedded Systems Design with Platform FPGAs

Design Methods and Techniques for Digital Circuits

The demand for broadband connectivity is growing rapidly, but cannot be met effectively by existing wireline technology. WiMAX has the potential to provide widespread Internet access that can

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usher in economic growth, better education and healthcare, and improved entertainment services. Examining the technology's global development and deployment a

This book covers the latest approaches and results from reconfigurable computing architectures employed in the finance domain. So-called field-programmable gate arrays (FPGAs) have already shown to outperform standard CPU- and GPU-based computing architectures by far, saving up to 99% of energy depending on the compute tasks. Renowned authors from financial mathematics, computer architecture and finance business introduce the readers into today's challenges in finance IT, illustrate the most advanced approaches and use cases and present currently known methodologies for integrating FPGAs in finance systems together with latest results. The complete

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algorithm-to-hardware flow is covered holistically, so this book serves as a hands-on guide for IT managers, researchers and quants/programmers who think about integrating FPGAs into their current IT systems.

This book helps readers to implement their designs on Xilinx® FPGAs. The authors demonstrate how to get the greatest impact from using the Vivado® Design Suite, which delivers a SoC-strength, IP-centric and system-centric, next generation development environment that has been built from the ground up to address the productivity bottlenecks in system-level integration and implementation. This book is a hands-on guide for both users who are new to FPGA designs, as well as those currently using the legacy Xilinx tool set (ISE) but are now moving to Vivado. Throughout the presentation, the authors focus on key concepts,

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major mechanisms for design entry, and methods to realize the most efficient implementation of the target design, with the least number of iterations.

HWM

Serial Attached SCSI

Trustworthy Computing and Services

6th International Workshop, PQCrypto 2014, Waterloo, ON, Canada, October 1-3, 2014. Proceedings

Sys Admin

All the PC essentials at your fingertips! If you like your answers quick and your information up-to-date, look no further. This concise, superbly organized reference walks you through setting up a PC, the new interface and features of Windows Vista, using basic applications, organizing your stuff, getting on the Internet,

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networking (wired and wireless), burning CDs, upgrading your hardware, revving up your PC for high-speed game playing, and much more!

A synergistic approach to signal integrity for high-speed digital design This book is designed to provide contemporary readers with an understanding of the emerging high-speed signal integrity issues that are creating roadblocks in digital design. Written by the foremost experts on the subject, it leverages concepts and techniques from non-related fields such as applied physics and microwave engineering and applies them to high-speed digital design—creating the optimal combination between theory and practical applications. Following an introduction to the importance of signal integrity, chapter coverage includes:

Electromagnetic fundamentals for signal integrity Transmission line

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fundamentals Crosstalk Non-ideal conductor models, including surface roughness and frequency-dependent inductance Frequency-dependent properties of dielectrics Differential signaling Mathematical requirements of physical channels S-parameters for digital engineers Non-ideal return paths and via resonance I/O circuits and models Equalization Modeling and budgeting of timing jitter and noise System analysis using response surface modeling Each chapter includes many figures and numerous examples to help readers relate the concepts to everyday design and concludes with problems for readers to test their understanding of the material. Advanced Signal Integrity for High-Speed Digital Designs is suitable as a textbook for graduate-level courses on signal integrity, for programs taught in industry for professional engineers, and as a reference for the high-speed

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digitaldesigner.

This book highlights the complex issues, tasks and skills that must be mastered by an IP designer, in order to design an optimized and robust digital circuit to solve a problem. The techniques and methodologies described can serve as a bridge between specifications that are known to the designer and RTL code that is final outcome, reducing significantly the time it takes to convert initial ideas and concepts into right-first-time silicon. Coverage focuses on real problems rather than theoretical concepts, with an emphasis on design techniques across various aspects of chip-design.

Feedback Systems

PCI System Architecture

100 Power Tips for FPGA Designers

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Introduction to PCI Express

Wi-MAX Monthly Newsletter

This book describes the most frequently used high-speed serial buses in embedded systems, especially those used by FPGAs. These buses employ SerDes, JESD204, SRIO, PCIE, Aurora and SATA protocols for chip-to-chip and board-to-board communication, and CPCIE, VPX, FC and Infiniband protocols for inter-chassis communication. For each type, the book provides the bus history and version info, while also assessing its advantages and limitations. Furthermore, it offers a detailed guide to implementing these buses in FPGA design, from the physical layer and

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link synchronization to the frame format and application command. Given its scope, the book offers a valuable resource for researchers, R&D engineers and graduate students in computer science or electronics who wish to learn the protocol principles, structures and applications of high-speed serial buses.

Focusing on the latest communication technologies, methods, and applications that can differentiate products and services, this book covers the essential elements of device and system development and shows how companies can deliver faster service without compromising quality. SAS (Serial Attached SCSI) is the serial storage interface

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that has been designed to replace and upgrade SCSI, by far the most popular storage interface for high-performance systems for many years. Retaining backward compatibility with the millions of lines of code written to support SCSI devices, SAS incorporates recent advances in high-speed serial design to provide better performance, better reliability and enhanced capabilities, all at a lower cost. SAS will be a significant part of many future high-performance storage systems, and hardware designers, system validation engineers, device driver developers and others working in this area will need a working knowledge of it. SAS Storage Architecture provides a comprehensive

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guide to the SAS standard. The book contains descriptions and numerous examples of the concepts presented, using the same building block approach as other MindShare offerings. This book details important concepts relating to the design and implementation of storage networks. Specific topics of interest include: SATA Compatibility Expander devices Discovery Process Connection protocols Arbitration of competing connection requests Flow Control protocols ACK/NAK protocol Primitives ? construction and uses Frames ? format, definition, used of each field Error checking mechanisms Description of responsibilities for each layer: Application

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layer ? mode and log pages Transport Layer ? frame construction Port Layer ? call center model Link Layer ? establish and maintain connections Phy Layer ? OOB, Initialization, and Reset Physical Layer ? connectors and cables Serial Support ? serial transmission support requirements The future of SAS ? competition with SATA and Fibre Channel in the server marketplace PCI Express System Architecture The Complete PCI Express Reference Design and Test for Multiple Gbps Communication Devices and Systems Principles and Practices

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A Hardware and Software Developer's Guide

Containing over 300 entries in an A-Z format, the Encyclopedia of Parallel Computing provides easy, intuitive access to relevant information for professionals and researchers seeking access to any aspect within the broad field of parallel computing. Topics for this comprehensive reference were selected, written, and peer-reviewed by an international pool of distinguished researchers in the field. The Encyclopedia is broad in scope, covering machine organization, programming languages, algorithms, and applications. Within each

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area, concepts, designs, and specific implementations are presented. The highly-structured essays in this work comprise synonyms, a definition and discussion of the topic, bibliographies, and links to related literature. Extensive cross-references to other entries within the Encyclopedia support efficient, user-friendly searches for immediate access to useful information. Key concepts presented in the Encyclopedia of Parallel Computing include; laws and metrics; specific numerical and non-numerical algorithms; asynchronous algorithms; libraries of subroutines; benchmark suites;

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applications; sequential consistency and cache coherency; machine classes such as clusters, shared-memory multiprocessors, special-purpose machines and dataflow machines; specific machines such as Cray supercomputers, IBM's cell processor and Intel's multicore machines; race detection and auto parallelization; parallel programming languages, synchronization primitives, collective operations, message passing libraries, checkpointing, and operating systems. Topics covered: Speedup, Efficiency, Isoefficiency, Redundancy, Amdahls law, Computer Architecture Concepts,

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Parallel Machine Designs, Benchmarks, Parallel Programming concepts & design, Algorithms, Parallel applications. This authoritative reference will be published in two formats: print and online. The online edition features hyperlinks to cross-references and to additional significant research. Related Subjects: supercomputing, high-performance computing, distributed computing Offering detailed interpretations of the PCI Express specifications, this reference for hardware and software developers compares features of PCI Express with PCI-X and PCI, discusses implications of the layered

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architecture of PCI Express, explains routing of transactions, looks at new form factors Offering an overview, this guide details how 3GIO allows designers to overcome the practical performance limits of existing multidrop, parallel bus technology and explains how to increase performance and new capabilities for a broad range of computing and communications platforms.

*High-speed Serial Buses in Embedded Systems
FPGA Based Accelerators for Financial Applications*

Using Vivado

VLSI Reference Circuits - Theory, Design, and

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Applications

Encyclopedia of Parallel Computing

***The Complete PCI Express Reference Design
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***This book constitutes the refereed
proceedings of the International Standard
Conference on Trustworthy Distributed
Computing and Services, ISCTCS 2013, held in
Beijing, China, in November 2013. The 49
revised full papers presented were carefully
reviewed and selected from 267 papers. The***

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***topics covered are trustworthy
infrastructure; security, survivability and
fault tolerance; standards, evaluation and
certification; trustworthiness of services.***

PCI-X System Architecture

Linux Device Drivers

PCs For Dummies Quick Reference